

II Year - I Semester

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## ELECTRONIC DEVICES AND CIRCUITS

### Objectives:

The main objectives of this course are:

- The basic concepts of semiconductor physics are to be reviewed.
- Study the physical phenomena such as conduction, transport mechanism and electrical characteristics of different diodes.
- The application of diodes as rectifiers with their operation and characteristics with and without filters are discussed.
- The principal of working and operation of Bipolar Junction Transistor and Field Effect Transistor and their characteristics are explained.
- The need of transistor biasing and its significance is explained. The quiescent point or operating point is explained.
- Small signal equivalent circuit analysis of BJT and FET transistor amplifiers in different configuration is explained.

### Syllabus:

**UNIT-I:Semi Conductor Physics :** Insulators, Semi conductors, and Metals classification using energy band diagrams, mobility and conductivity, electrons and holes in intrinsic semi conductors, extrinsic semi conductors, drift and diffusion, charge densities in semiconductors, Hall effect, continuity equation, law of junction, Fermi Dirac function, Fermi level in intrinsic and extrinsic Semiconductors

**UNIT- II: Junction Diode Characteristics :** Open circuited p-n junction, Biased p-n junction, p-n junction diode, current components in PN junction Diode, diode equation, V-I Characteristics, temperature dependence on V-I characteristics, Diode resistance, Diode capacitance, energy band diagram of PN junction Diode.

**Special Semiconductor Diodes:** Zener Diode, Breakdown mechanisms, Zener diode applications, LED, Photo diode, Tunnel Diode, SCR, UJT. Construction, operation and characteristics of all the diodes are required to be considered.

**UNIT- III: Rectifiers and Filters:** Basic Rectifier setup, half wave rectifier, full wave rectifier, bridge rectifier, derivations of characteristics of rectifiers, rectifier circuits-operation, input and output waveforms, Filters, Inductor filter, Capacitor filter, comparison of various filter circuits in terms of ripple factors.

**UNIT- IV: Transistor Characteristics:**

**BJT:** Junction transistor, transistor current components, transistor equation, transistor configurations, transistor as an amplifier, characteristics of transistor in Common Base, Common Emitter and Common Collector configurations, Ebers-Moll model of a transistor, punch through/ reach through, Photo transistor, typical transistor junction voltage values.

**FET:** FET types, construction, operation, characteristics, parameters, MOSFET-types, construction, operation, characteristics, comparison between JFET and MOSFET.

**UNIT- V: Transistor Biasing and Thermal Stabilization :** Need for biasing, operating point, load line analysis, BJT biasing- methods, basic stability, fixed bias, collector to base bias, self bias, Stabilization against variations in  $V_{BE}$ ,  $I_c$ , and  $\beta$ , Stability factors, ( $S$ ,  $S'$ ,  $S''$ ), Bias compensation, Thermal runaway, Thermal stability.

FET Biasing- methods and stabilization.

**UNIT- VI: Small Signal Low Frequency Transistor Amplifier Models:**

**BJT:** Two port network, Transistor hybrid model, determination of h-parameters, conversion of h-parameters, generalized analysis of transistor amplifier model using h-parameters, Analysis of CB, CE and CC amplifiers using exact and approximate analysis, Comparison of transistor amplifiers.

**FET:** Generalized analysis of small signal model, Analysis of CG, CS and CD amplifiers, comparison of FET amplifiers.

**Text Books:**

1. Electronic Devices and Circuits- J. Millman, C. Halkias, Tata Mc-Graw Hill, Second Edition.
2. Integrated Electronics- Jacob Millman, C. Halkies, C.D.Parikh, Tata Mc-Graw Hill, 2009.

**References:**

1. Electronic Devices and Circuits-K. Satya Prasad, VGS Book Links.
2. Electronic Devices and Circuits-Salivahanan, Kumar, Vallavaraj, Tata Mc-Graw Hill, Second Edition
3. Electronic Devices and Circuits – Bell, Oxford

**Outcomes:**

**At the end of this course the student can able to:**

- Understand the basic concepts of semiconductor physics.
- Understand the formation of p-n junction and how it can be used as a p-n junction as diode in different modes of operation.
- Know the construction, working principle of rectifiers with and without filters with relevant expressions and necessary comparisons.
- Understand the construction, principle of operation of transistors, BJT and FET with their V-I characteristics in different configurations.
- Know the need of transistor biasing, various biasing techniques for BJT and FET and stabilization concepts with necessary expressions.
- Perform the analysis of small signal low frequency transistor amplifier circuits using BJT and FET in different configurations.

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**SWITCHING THEORY AND LOGIC DESIGN**

**UNIT – I: REVIEW OF NUMBER SYSTEMS & CODES:**

- i) Representation of numbers of different radix, conversion from one radix to another radix,  $r-1$ 's complements and  $r$ 's complements of signed members, problem solving.
- ii) 4 bit codes, BCD, Excess-3, 2421, 84-2-1  $9$ 's complement code etc.,
- iii) Logic operations and error detection & correction codes; Basic logic operations -NOT, OR, AND, Universal building blocks, EX-OR, EX-NOR - Gates, Standard SOP and POS, Forms, Gray code, error detection, error correction codes (parity checking, even parity, odd parity, Hamming code) NAND-NAND and NOR-NOR realizations.

**UNIT – II: MINIMIZATION TECHNIQUES**

Boolean theorems, principle of complementation & duality, De-morgan theorems, minimization of logic functions using Boolean theorems, minimization of switching functions using K-Map up to 6 variables, tabular minimization, problem solving (code-converters using K-Map etc..).

**UNIT – III: COMBINATIONAL LOGIC CIRCUITS DESIGN**

Design of Half adder, full adder, half subtractor, full subtractor, applications of full adders, 4-bit binary subtractor, adder-subtractor circuit, BCD adder circuit, Excess 3 adder circuit, look-a-head adder circuit, Design of decoder, demultiplexer, 7 segment decoder, higher order demultiplexing, encoder, multiplexer, higher order multiplexing, realization of Boolean functions using decoders and multiplexers, priority encoder, 4-bit digital comparator.

**UNIT – IV: INTRODUCTION OF PLD's**

PROM, PAL, PLA-Basics structures, realization of Boolean function with PLDs, programming tables of PLDs, merits & demerits of PROM, PAL, PLA comparison, realization of Boolean functions using PROM, PAL, PLA, programming tables of PROM, PAL, PLA.

**UNIT – V: SEQUENTIAL CIRCUITS I**

Classification of sequential circuits (synchronous and asynchronous); basic flip-flops, truth tables and excitation tables (nand RS latch, nor RS latch, RS flip-flop, JK flip-flop, T flip-flop, D flip-flop with reset and clear terminals). Conversion from one flip-flop to flip-flop. Design of ripple counters, design of synchronous counters, Johnson counter, ring counter. Design of registers - Buffer register, control buffer register, shift register, bi-directional shift register, universal shift register.

**UNIT – VI: SEQUENTIAL CIRCUITS II**

Finite state machine; Analysis of clocked sequential circuits, state diagrams, state tables, reduction of state tables and state assignment, design procedures. Realization of circuits using various flip-flops. Mealy to Moore conversion and vice-versa.

**TEXT BOOKS:**

1. Switching Theory and Logic Design by Hill and Peterson Mc-Graw Hill TMH edition.
2. Switching Theory and Logic Design by A. Anand Kumar
3. Digital Design by Mano PHI.

**REFERENCE BOOKS:**

1. Modern Digital Electronics by RP Jain, TMH
2. Fundamentals of Logic Design by Charles H. Roth Jr, Jaico Publishers
3. Micro electronics by Milliman MH edition.

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## SIGNALS & SYSTEMS

### OBJECTIVES:

The main objectives of this course are given below:

- To introduce the terminology of signals and systems.
- To introduce Fourier tools through the analogy between vectors and signals.
- To introduce the concept of sampling and reconstruction of signals.
- To analyze the linear systems in time and frequency domains.
- To study z-transform as mathematical tool to analyze discrete-time signals and systems.

**UNIT- I: INTRODUCTION:** Definition of Signals and Systems, Classification of Signals, Classification of Systems, Operations on signals: time-shifting, time-scaling, amplitude-shifting, amplitude-scaling. Problems on classification and characteristics of Signals and Systems. Complex exponential and sinusoidal signals, Singularity functions and related functions: impulse function, step function signum function and ramp function. Analogy between vectors and signals, orthogonal signal space, Signal approximation using orthogonal functions, Mean square error, closed or complete set of orthogonal functions, Orthogonality in complex functions.

### UNIT –II: FOURIER SERIES AND FOURIER TRANSFORM:

Fourier series representation of continuous time periodic signals, properties of Fourier series, Dirichlet's conditions, Trigonometric Fourier series and Exponential Fourier series, Complex Fourier spectrum. Deriving Fourier transform from Fourier series, Fourier transform of arbitrary signal, Fourier transform of standard signals, Fourier transform of periodic signals, properties of Fourier transforms, Fourier transforms involving impulse function and Signum function. Introduction to Hilbert Transform.

**UNIT –III: SAMPLING THEOREM** – Graphical and analytical proof for Band Limited Signals, impulse sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, effect of under sampling – Aliasing, Introduction to Band Pass sampling.

**UNIT-IV: ANALYSIS OF LINEAR SYSTEMS:** Linear system, impulse response, Response of a linear system, Linear time invariant (LTI) system, Linear time variant (LTV) system, Concept of convolution in time domain and frequency domain, Graphical representation of convolution, Transfer function of a LTI system. Filter characteristics of linear systems. Distortion less transmission through a system, Signal bandwidth, system bandwidth, Ideal LPF, HPF and BPF characteristics, Causality and Poly-Wiener criterion for physical realization, relationship between bandwidth and rise time.

Cross-correlation and auto-correlation of functions, properties of correlation function, Energy density spectrum, Parseval's theorem, Power density spectrum, Relation between auto correlation function and energy/power spectral density function. Relation between convolution and correlation, Detection of periodic signals in the presence of noise by correlation, Extraction of signal from noise by filtering.

**UNIT –V: LAPLACE TRANSFORMS :** Review of Laplace transforms, Partial fraction expansion, Inverse Laplace transform, Concept of region of convergence (ROC) for Laplace transforms, constraints on ROC for various classes of signals, Properties of L.T's, Relation between L.T's, and F.T. of a signal. Laplace transform of certain signals using waveform synthesis.

**UNIT –VI: Z–TRANSFORMS** : Fundamental difference between continuous-time and discrete-time signals, discrete time signal representation using complex exponential and sinusoidal components, Periodicity of discrete time using complex exponential signal, Concept of Z- Transform of a discrete sequence. Distinction between Laplace, Fourier and Z transforms. Region of convergence in Z-Transform, constraints on ROC for various classes of signals, Inverse Z-transform, properties of Z-transforms.

**TEXT BOOKS:**

1. Signals, Systems & Communications - B.P. Lathi, BS Publications, 2003.
2. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawab, PHI, 2nd Edn.
3. Signals & Systems- Narayan Iyer and K Satya Prasad, Cenage Pub.

**REFERENCE BOOKS:**

1. Signals & Systems - Simon Haykin and Van Veen, Wiley, 2nd Edition.
2. Principles of Linear Systems and Signals – BP Lathi, Oxford University Press, 2015
3. Signals and Systems – K Raja Rajeswari, B VisweswaraRao, PHI, 2009
4. Fundamentals of Signals and Systems- Michel J. Robert, MGH International Edition, 2008.
5. Signals and Systems – T K Rawat , Oxford University press, 2011

**OUTCOMES:**

**At the end of this course the student will able to:**

- Characterize the signals and systems and principles of vector spaces, Concept of orthogonality.
- Analyze the continuous-time signals and continuous-time systems using Fourier series, Fourier transform and Laplace transform.
- Apply sampling theorem to convert continuous-time signals to discrete-time signal and reconstruct back.
- Understand the relationships among the various representations of LTI systems
- Understand the Concepts of convolution, correlation, Energy and Power density spectrum and their relationships.
- Apply z-transform to analyze discrete-time signals and systems.

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## NETWORK ANALYSIS

### UNIT – I

**Introduction to Electrical Circuits :** Network elements classification, Electric charge and current, Electric energy and potential, Resistance parameter – series and parallel combination, Inductance parameter – series and parallel combination, Capacitance parameter – series and parallel combination. Energy sources: Ideal, Non-ideal, Independent and dependent sources, Source transformation, Kirchoff's laws, Mesh analysis and Nodal analysis problem solving with resistances only including dependent sources also. (Text Books: 1,2,3, Reference Books: 3)

**A.C Fundamentals and Network Topology:** Definitions of terms associated with periodic functions: Time period, Angular velocity and frequency, RMS value, Average value, Form factor and peak factor- problem solving, Phase angle, Phasor representation, Addition and subtraction of phasors, mathematical representation of sinusoidal quantities, explanation with relevant theory, problem solving. Principal of Duality with examples.

**Network Topology:** Definitions of branch, node, tree, planar, non-planar graph, incidence matrix, basic tie set schedule, basic cut set schedule. (Text Books: 2,3, Reference Books: 3)

### UNIT – II

**Steady State Analysis of A.C Circuits :** Response to sinusoidal excitation - pure resistance, pure inductance, pure capacitance, impedance concept, phase angle, series R-L, R-C, R-L-C circuits problem solving. Complex impedance and phasor notation for R-L, R-C, R-L-C problem solving using mesh and nodal analysis, Star-Delta conversion, problem solving. (Text Books: 1,2, Reference Books: 3)

### UNIT – III

**Coupled Circuits :** Coupled Circuits: Self inductance, Mutual inductance, Coefficient of coupling, analysis of coupled circuits, Natural current, Dot rule of coupled circuits, Conductively coupled equivalent circuits- problem solving.

**Resonance:** Introduction, Definition of Q, Series resonance, Bandwidth of series resonance, Parallel resonance, Condition for maximum impedance, current in anti resonance, Bandwidth of parallel resonance, general case-resistance present in both branches, anti resonance at all frequencies. (Text Books:2,3, Reference Books: 3)

### UNIT – IV

**Network Theorems:** Thevinin's, Norton's, Milliman's, Reciprocity, Compensation, Substitution, Superposition, Max Power Transfer, Tellegens- problem solving using dependent sources also. (Text Books: 1,2,3, Reference Books: 2)

### UNIT – V

**Two-port networks :** Relationship of two port networks, Z-parameters, Y-parameters, Transmission line parameters, h-parameters, Inverse h-parameters, Inverse Transmission line parameters, Relationship between parameter sets, Parallel connection of two port networks, Cascading of two port networks, series connection of two port networks, problem solving including dependent sources also. (Text Books: 1,2, Reference Books: 1,3)

## **UNIT – VI**

**Transients :** First order differential equations, Definition of time constants, R-L circuit, R-C circuit with DC excitation, Evaluating initial conditions procedure, second order differential equations, homogeneous, non-homogenous, problem solving using R-L-C elements with DC excitation and AC excitation, Response as related to s-plane rotation of roots. Solutions using Laplace transform method. (Text Books: 1,2,3, Reference Books: 1,3)

### **TEXT BOOKS:**

1. Network Analysis – ME Van Valkenburg, Prentice Hall of India, 3rd Edition, 2000.
2. Network Analysis by K.Satya Prasad and S Sivanagaraju, Cengage Learning
3. Electric Circuit Analysis by Hayt and Kimmarle, TMH

### **REFERENCES:**

1. Network lines and Fields by John. D. Ryder 2<sup>nd</sup> edition, Asia publishing house.
2. Basic Circuit Analysis by DR Cunningham, Jaico Publishers.
3. Network Analysis and Filter Design by Chadha, Umesh Publications.

### **COURSE OBJECTIVES:**

1. To understand the basic concepts on RLC circuits.
2. To know the behavior of the steady states and transients states in RLC circuits.
3. To know the basic Laplace transforms techniques in periods' waveforms.
4. To understand the two port network parameters.
5. To understand the properties of LC networks and filters.

### **COUSE OUTCOME:**

1. gain the knowledge on basic network elements.
2. will analyze the RLC circuits behavior in detailed.
3. analyze the performance of periodic waveforms.
4. gain the knowledge in characteristics of two port network parameters (Z, Y, ABCD, h & g).
5. analyze the filter design concepts in real world applications.



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## RANDOM VARIABLES & STOCHASTIC PROCESSES

### OBJECTIVES:

- To give students an introduction to elementary probability theory, in preparation for courses on statistical analysis, random variables and stochastic processes.
- To mathematically model the random phenomena with the help of probability theory concepts.
- To introduce the important concepts of random variables and stochastic processes.
- To analyze the LTI systems with stationary random process as input.
- To introduce the types of noise and modelling noise sources.

### UNIT I

**THE RANDOM VARIABLE :** Introduction, Review of Probability Theory, Definition of a Random Variable, Conditions for a Function to be a Random Variable, Discrete, Continuous and Mixed Random Variables, Distribution and Density functions, Properties, Binomial, Poisson, Uniform, Gaussian, Exponential, Rayleigh, Conditional Distribution, Conditional Density, Properties.

### UNIT II

**OPERATION ON ONE RANDOM VARIABLE – EXPECTATIONS :** Introduction, Expected Value of a Random Variable, Function of a Random Variable, Moments about the Origin, Central Moments, Variance and Skew, Chebychev's Inequality, Characteristic Function, Moment Generating Function, Transformations of a Random Variable: Monotonic Transformations for a Continuous Random Variable, Nonmonotonic Transformations of Continuous Random Variable.

### UNIT III

**MULTIPLE RANDOM VARIABLES :** Vector Random Variables, Joint Distribution Function, Properties of Joint Distribution, Marginal Distribution Functions, Conditional Distribution and Density, Statistical Independence, Sum of Two Random Variables, Sum of Several Random Variables, Central Limit Theorem: Unequal Distribution, Equal Distributions.

**OPERATIONS ON MULTIPLE RANDOM VARIABLES:** Joint Moments about the Origin, Joint Central Moments, Joint Characteristic Functions, Jointly Gaussian Random Variables: Two Random Variables case, N Random Variables case, Properties, Transformations of Multiple Random Variables, Linear Transformations of Gaussian Random Variables.

### UNIT IV

**RANDOM PROCESSES – TEMPORAL CHARACTERISTICS:** The Random Process Concept, Classification of Processes, Deterministic and Nondeterministic Processes, Distribution and Density Functions, Concept of Stationarity and Statistical Independence. First-Order Stationary Processes, Second-order and Wide-Sense Stationarity,  $N^{\text{th}}$ -order and Strict-Sense Stationarity, Time Averages and Ergodicity, Autocorrelation Function and its Properties, Cross-Correlation Function and its Properties, Covariance Functions, Gaussian Random Processes, Poisson Random Process.

### UNIT V

**RANDOM PROCESSES – SPECTRAL CHARACTERISTICS:** The Power Density Spectrum: Properties, Relationship between Power Density Spectrum and Autocorrelation Function, The Cross-Power Density Spectrum, Properties, Relationship between Cross-Power Density Spectrum and Cross-Correlation Function.

## **UNIT VI**

**LINEAR SYSTEMS WITH RANDOM INPUTS :** Random Signal Response of Linear Systems: System Response – Convolution, Mean and Mean-squared Value of System Response, Autocorrelation Function of Response, Cross-Correlation Functions of Input and Output, Spectral Characteristics of System Response: Power Density Spectrum of Response, Cross-Power Density Spectra of Input and Output, Band pass, Band-Limited and Narrowband Processes, Properties, Modeling of Noise Sources: Resistive (Thermal) Noise Source, Arbitrary Noise Sources, Effective Noise Temperature, Average Noise Figure, Average Noise Figure of cascaded networks.

### **TEXT BOOKS:**

1. Probability, Random Variables & Random Signal Principles, Peyton Z. Peebles, TMH, 4<sup>th</sup> Edition, 2001.
2. Probability, Random Variables and Stochastic Processes, Athanasios Papoulis and S.Unnikrishna, PHI, 4<sup>th</sup> Edition, 2002.

### **REFERENCE BOOKS:**

1. Probability Theory and Stochastic Processes – B. Prabhakara Rao, BS Publications
2. Probability and Random Processes with Applications to Signal Processing, Henry Stark and John W. Woods, Pearson Education, 3<sup>rd</sup> Edition.
3. Schaum's Outline of Probability, Random Variables, and Random Processes.
4. An Introduction to Random Signals and Communication Theory, B.P. Lathi, International Textbook, 1968.
5. Random Process – Ludeman , John Wiley
6. Probability Theory and Random Processes, P. Ramesh Babu, McGrawHill, 2015.

### **OUTCOMES:**

**After completion of the course, the student will be able to**

- Mathematically model the random phenomena and solve simple probabilistic problems.
- Identify different types of random variables and compute statistical averages of these random variables.
- Characterize the random processes in the time and frequency domains.
- Analyze the LTI systems with random inputs.
- Apply these techniques to analyze the systems in the presence of different types of noise.

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## MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS

(Common to all Branches)

### • Course Objectives:

- The Learning objectives of this paper is to understand the concept and nature of Managerial Economics and its relationship with other disciplines and also to understand the Concept of Demand and Demand forecasting, Production function, Input Output relationship, Cost-Output relationship and Cost-Volume-Profit Analysis.
- To understand the nature of markets, Methods of Pricing in the different market structures and to know the different forms of Business organization and the concept of Business Cycles.
- To learn different Accounting Systems, preparation of Financial Statement and uses of different tools for performance evaluation. Finally, it is also to understand the concept of Capital, Capital Budgeting and the techniques used to evaluate Capital Budgeting proposals.

### UNIT-I

#### **Introduction to Managerial Economics and demand Analysis:**

Definition of Managerial Economics –Scope of Managerial Economics and its relationship with other subjects – Concept of Demand, Types of Demand, Determinants of Demand- Demand schedule, Demand curve, Law of Demand and its limitations- Elasticity of Demand, Types of Elasticity of Demand and Measurement- Demand forecasting and Methods of forecasting..

### UNIT – II

#### **Production and Cost Analyses:**

Concept of Production function- Cobb-Douglas Production function- Leontief production function - Law of Variable proportions-Isoquants and Isocosts and choice of least cost factor combination-Concepts of Returns to scale and Economies of scale-Different cost concepts: opportunity costs, explicit and implicit costs- Fixed costs, Variable Costs and Total costs –Cost –Volume-Profit analysis-Determination of Breakeven point(simple problems)- Managerial significance and limitations of Breakeven point.

### UNIT – III

#### **Introduction to Markets, Theories of the Firm & Pricing Policies:**

Market Structures: Perfect Competition, Monopoly, Monopolistic competition and Oligopoly – Features – Price and Output Determination – Managerial Theories of firm: Marris and Williamson's models – other Methods of Pricing: Average cost pricing, Limit Pricing, Market Skimming Pricing, Internet Pricing: Flat Rate Pricing, Usage sensitive pricing and Priority Pricing.

### UNIT – IV

#### **Types of Business Organization and Business Cycles:**

Features and Evaluation of Sole Trader, Partnership, Joint Stock Company – State/Public Enterprises and their forms – Business Cycles : Meaning and Features – Phases of Business Cycle.

### UNIT – V

#### **Introduction to Accounting & Financing Analysis:**

Introduction to Double Entry Systems – Preparation of Financial Statements-Analysis and Interpretation of Financial Statements-Ratio Analysis – Preparation of Funds flow and cash flow statements (Simple Problems)

## **UNIT – VI**

**Capital and Capital Budgeting:** Capital Budgeting: Meaning of Capital-Capitalization-Meaning of Capital Budgeting-Time value of money- Methods of appraising Project profitability: Traditional Methods(pay back period, accounting rate of return) and modern methods(Discounted cash flow method, Net Present Value method, Internal Rate of Return Method and Profitability Index)

### **Course Outcome:**

- \*The Learner is equipped with the knowledge of estimating the Demand and demand elasticities for a product and the knowledge of understanding of the Input-Output-Cost relationships and estimation of the least cost combination of inputs.
- \* One is also ready to understand the nature of different markets and Price Output determination under various market conditions and also to have the knowledge of different Business Units.
- \*The Learner is able to prepare Financial Statements and the usage of various Accounting tools for Analysis and to evaluate various investment project proposals with the help of capital budgeting techniques for decision making.

### **TEXT BOOKS**

1. Dr. N. AppaRao, Dr. P. Vijay Kumar: 'Managerial Economics and Financial Analysis', Cengage Publications, New Delhi – 2011
2. Dr. A. R. Aryasri – Managerial Economics and Financial Analysis, TMH 2011
3. Prof. J.V.Prabhakararao, Prof. P. Venkatarao. 'Managerial Economics and Financial Analysis', Ravindra Publication.

### **REFERENCES:**

1. Dr. B. Kuberudu and Dr. T. V. Ramana: Managerial Economics & Financial Analysis, Himalaya Publishing House, 2014.
2. V. Maheswari: Managerial Economics, Sultan Chand.2014
3. Suma Damodaran: Managerial Economics, Oxford 2011.
4. VanithaAgarwal: Managerial Economics, Pearson Publications 2011.
5. Sanjay Dhameja: Financial Accounting for Managers, Pearson.
6. Maheswari: Financial Accounting, Vikas Publications.
7. S. A. Siddiqui& A. S. Siddiqui: Managerial Economics and Financial Analysis, New Age International Publishers, 2012
8. Ramesh Singh, Indian Economy, 7<sup>th</sup> Edn., TMH2015
9. Pankaj Tandon A Text Book of Microeconomic Theory, Sage Publishers, 2015
10. Shailaja Gajjala and Usha Munipalle, Univerties press, 2015

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**ELECTRONIC DEVICES AND CIRCUITS LAB**

**Note:** The students are required to perform the experiment to obtain the V-I characteristics and to determine the relevant parameters from the obtained graphs.

**Electronic Workshop Practice:**

1. Identification, Specifications, Testing of R, L, C Components (Colour Codes), Potentiometers, Coils, Gang Condensers, Relays, Bread Boards.
2. Identification, Specifications and Testing of active devices, Diodes, BJTs, JFETs, LEDs, LCDs, SCR, UJT.
3. Soldering Practice- Simple circuits using active and passive components.
4. Study and operation of Ammeters, Voltmeters, Transformers, Analog and Digital Multimeter, Function Generator, Regulated Power Supply and CRO..

**List of Experiments: (Minimum of Ten Experiments has to be performed)**

1. P-N Junction Diode Characteristics  
Part A: Germanium Diode (Forward bias & Reverse bias)  
Part B: Silicon Diode (Forward Bias only)
2. Zener Diode Characteristics  
Part A: V-I Characteristics  
Part B: Zener Diode as Voltage Regulator
3. Rectifiers (without and with c-filter)  
Part A: Half-wave Rectifier  
Part B: Full-wave Rectifier
4. BJT Characteristics(CE Configuration)  
Part A: Input Characteristics  
Part B: Output Characteristics
5. FET Characteristics(CS Configuration)  
Part A: Drain Characteristics  
Part B: Transfer Characteristics
6. SCR Characteristics
7. UJT Characteristics
8. Transistor Biasing
9. CRO Operation and its Measurements
10. BJT-CE Amplifier
11. Emitter Follower-CC Amplifier
12. FET-CS Amplifier

**Equipment required:**

1. Regulated Power supplies
2. Analog/Digital Storage Oscilloscopes
3. Analog/Digital Function Generators
4. Digital Multimeters
5. Decade Résistance Boxes/Rheostats
6. Decade Capacitance Boxes
7. Ammeters (Analog or Digital)
8. Voltmeters (Analog or Digital)
9. Active & Passive Electronic Components

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### NETWORKS & ELECTRICAL TECHNOLOGY LAB

#### Learning Objectives:

- To determine resonance frequency, Q-factor of RLC network.
- To analysis time response of first orders RC/RL network for non-sinusoidal inputs.
- To estimate parameters of two port networks
- To understand the concept network theorems in network reduction of electrical networks.
- To determine efficiency of dc shunt machine with actual loading.
- To analyse performance of 3 phase induction motor
- To understand the significance of regulation of an alternators through synchronous impedance method.

#### PART – A

##### Any five experiments are to be conducted from each part

1. Series and Parallel Resonance – Timing, Resonant frequency, Bandwidth and Q-factor determination for RLC network.
2. Time response of first order RC/RL network for periodic non-sinusoidal inputs – time constant and steady state error determination.
3. Two port network parameters – Z-Y Parameters, chain matrix and analytical verification.
4. Verification of Superposition and Reciprocity theorems.
5. Verification of maximum power transfer theorem. Verification on DC, verification on AC with Resistive and Reactive loads.
6. Experimental determination of Thevenin's and Norton's equivalent circuits and verification by direct test.

#### PART – B

1. Magnetization characteristics of D.C. Shunt generator. Determination of critical field resistance.
2. Speed control of D.C. Shunt motor by Armature & flux control methods
3. Brake test on DC shunt motor. Determination of performance characteristics.
4. OC & SC tests on Single-phase transformer (Predetermination of efficiency and regulation at given power factors and determination of equivalent circuit).
5. Brake test on 3-phase Induction motor (performance characteristics).
6. Regulation of alternator by synchronous impedance method

#### Learning Outcomes:

- Able to analyse RLC circuits and understand resonant frequency and Q-factor.
- Able to determine first order RC/RL networks of periodic non- sinusoidal waveforms.
- Able to apply network theorems to analyze the electrical network.
- Able to describe the performance of dc shunt machine.
- Able to investigate the performance of 1-phase transformer.
- Able to perform tests on 3-phase induction motor and alternator to determine their performance characteristic

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## ELECTRONIC CIRCUIT ANALYSIS

### Objectives:

The main objectives of this course are:

- Small signal high frequency BJT transistor amplifier Hybrid- $\pi$  equivalent circuit and the expressions for conductances and capacitances are derived.
- Cascading of single stage amplifiers is discussed. Expressions for overall voltage gain are derived.
- The concept of feedback is introduced. Effect of negative feedback on amplifier characteristics is explained and necessary equations are derived.
- Basic principle of oscillator circuits is explained and different oscillator circuits are given with their analysis.
- Power amplifiers Class A, Class B, Class C, Class AB and other types of amplifiers are analyzed.
- Different types of tuned amplifier circuits are analyzed.

### Outcomes:

At the end of this course the student can able to:

- Design and analysis of small signal high frequency transistor amplifier using BJT and FET.
- Design and analysis of multi stage amplifiers using BJT and FET and Differential amplifier using BJT
- Derive the expressions for frequency of oscillation and condition for oscillation of RC and LC oscillators and their amplitude and frequency stability concept.
- Know the classification of the power and tuned amplifiers and their analysis with performance comparison.

### Syllabus:

#### UNIT-I Small Signal High Frequency Transistor Amplifier models:

**BJT:** Transistor at high frequencies, Hybrid-  $\pi$  common emitter transistor model, Hybrid  $\pi$  conductances, Hybrid  $\pi$  capacitances, validity of hybrid  $\pi$  model, determination of high-frequency parameters in terms of low-frequency parameters, CE short circuit current gain, current gain with resistive load, cut-off frequencies, frequency response and gain bandwidth product.

**FET:** Analysis of common Source and common drain Amplifier circuits at high frequencies.

#### UNIT-II

**Multistage Amplifiers :** Classification of amplifiers, methods of coupling, cascaded transistor amplifier and its analysis, analysis of two stage RC coupled amplifier, high input resistance transistor amplifier circuits and their analysis-Darlington pair amplifier, Cascode amplifier, Boot-strap emitter follower, Analysis of multi stage amplifiers using FET, Differential amplifier using BJT.



### **UNIT -III**

**Feedback Amplifiers :** Feedback principle and concept, types of feedback, classification of amplifiers, feedback topologies, Characteristics of negative feedback amplifiers, Generalized analysis of feedback amplifiers, Performance comparison of feedback amplifiers, Method of analysis of feedback amplifiers.

### **Unit-IV**

**Oscillators:** Oscillator principle, condition for oscillations, types of oscillators, RC-phase shift and Wein bridge oscillators with BJT and FET and their analysis, Generalized analysis of LC Oscillators, Hartley and Colpitt's oscillators with BJT and FET and their analysis, Frequency and amplitude stability of oscillators.

### **UNIT-V**

**Power Amplifiers:** Classification of amplifiers, Class A power Amplifiers and their analysis, Harmonic Distortions, Class B Push-pull amplifiers and their analysis, Complementary symmetry push pull amplifier, Class AB power amplifier, Class-C power amplifier, Thermal stability and Heat sinks, Distortion in amplifiers.

### **UNIT-VI**

**Tuned Amplifiers :** Introduction, Q-Factor, small signal tuned amplifier, capacitance single tuned amplifier, double tuned amplifiers, effect of cascading single tuned amplifiers on band width, effect of cascading double tuned amplifiers on band width, staggered tuned amplifiers, stability of tuned amplifiers, wideband amplifiers.

### **Text Books:**

1. Integrated Electronics- J. Millman and C.C. Halkias, Tata Mc Graw-Hill, 1972.
2. Electronic Devices and Circuits- Salivahanan, N.Suresh Kumar, A. Vallavaraj, TATA McGraw Hill, Second Edition

### **References:**

1. Electronic Circuit Analysis and Design – Donald A. Neaman, Mc Graw Hill.
2. Electronic Devices and Circuits Theory – Robert L. Boylestad and Louis Nashelsky, Pearson/Prentice Hall, Tenth Edition.
3. Electronic Circuit Analysis-B.V.Rao,K.R.Rajeswari, P.C.R.Pantulu,K.B.R.Murthy, Pearson Publications.
4. Microelectronic Circuits-Sedra A.S. and K.C. Smith, Oxford University Press, Sixth Edition.

II Year - II Semester

L	T	P	C
4	0	0	3

## CONTROL SYSTEMS

### Course objectives

1. To introduce the concepts of open loop and closed loop systems, mathematical models of mechanical and electrical systems, and concepts of feedback
2. To study the characteristics of the given system in terms of the transfer function and introducing various approaches to reduce the overall system for necessary analysis
3. To develop the acquaintance in analyzing the system response in time-domain and frequency domain in terms of various performance indices
4. To analyze the system in terms of absolute stability and relative stability by different approaches
5. To design different control systems for different applications as per given specifications
6. To introduce the concepts of state variable analysis, design and also the concepts of controllability and observability

### UNIT-1

#### Introduction

System Control System, Open Loop Control System, Closed loop Control System, Different Examples

#### Mathematical models of Physical Systems

Differential equations of physical systems, Transfer functions, Block diagram Algebra, Signal flow graphs with illustrative examples

#### Effects of Feedback

Feedback Characteristics and its advantages, Linearizing effect of feedback

### UNIT-2

#### Controller Components

DC Servomotor (Armature Controlled and Field Controlled) with necessary derivation for transfer function, AC Servomotor and its transfer function, AC Tachometer, Potentiometer, Synchros, AC Position Control Systems

#### Time Response Analysis

Standard test Signals, Time response of first and second order systems, steady state errors and error constants, Effect of adding a zero to a system, Design specifications of second order systems, Performance indices

### UNIT-3

#### Concepts of Stability and Algebraic Criteria

The concept of Stability, Necessary Conditions for Stability, Routh-Hurwitz Stability Criterion, Relative stability analysis,

#### The Root Locus Technique

Introduction, The Root Locus concepts, Construction of Root Loci

## **UNIT-4**

### **Frequency response analysis**

Introduction, Correlation between time and frequency response, Polar Plots, Bode Plots, Nyquist Stability Criterion

## **UNIT-5**

### **Introduction to Design**

The design problem, Preliminary consideration of classical design, Realization of basic Compensators, Cascade compensation in time domain and frequency domain, Tuning of PID Controllers

## **UNIT-6**

### **State Variable Analysis and Design**

Introduction, Concepts of State, State Variables and State models, State models for linear continuous-time systems, State variables and linear discrete-time systems, Solution of state equations and Concepts of Controllability and Observability.

### **Text Book**

I.J.Nagarath and M.Gopal, “ **Control System Engineering,**” New Age International Publishers, Fifth Edition

### **Reference Books**

1. Katsuhiko Ogata, “Modern Control Engineering,” Pearson, Fifth Edition
2. S. Salivahanan, R. Rengaraj, and G. R. Venkata Krishnan, “ Control Systems Engineering,” Pearson, First Impression
3. Benjamin C. Kuo, Farid Golnaraghi, “ Automatic Control Systems,” Wiley Student Edition, Eighth Edition
4. PadmaRaju and Reddy , “ Instrumentation and Control Systems “, McGrawHill Education ,2016

### **Course Outcomes**

1. This course introduces the concepts of feedback and its advantages to various control systems
2. The performance metrics to design the control system in time-domain and frequency domain are introduced.
3. Control systems for various applications can be designed using time-domain and frequency domain analysis.
4. In addition to the conventional approach, the state space approach for the analysis of control systems is also introduced.

II Year - II Semester

L	T	P	C
4	0	0	3

## EM WAVES AND TRANSMISSION LINES

### OBJECTIVES:

#### The main objectives of this course are to understand:

1. Fundamentals of steady electric and magnetic fields using various laws
2. The concept of static and time varying Maxwell equations and power flow using pointing theorem
3. Wave characteristics in different media for normal and oblique incidence
4. Various concepts of transmission lines and impedance measurements

### SYLLABUS:

#### UNIT I:

Review of Co-ordinate Systems, **Electrostatics:**, Coulomb's Law, Electric Field Intensity, Electric Flux Density, Gauss Law and Applications, Electric Potential, Maxwell's Two Equations for Electrostatic Fields, Energy Density, Illustrative Problems. Convection and Conduction Currents, Dielectric Constant, Continuity Equation, Relaxation Time, Poisson's and Laplace's Equations; Capacitance – Parallel Plate, Coaxial Capacitors, Illustrative Problems. [1,5]

**UNIT II: Magneto Statics :** Biot-Savart Law, Ampere's Circuital Law and Applications, Magnetic Flux Density, Maxwell's Two Equations for Magnetostatic Fields, Magnetic Scalar and Vector Potentials, Forces due to Magnetic Fields, Ampere's Force Law, Inductances and Magnetic Energy. Illustrative Problems. [1,5]

**Maxwell's Equations (Time Varying Fields):** Faraday's Law and Transformer emf, Inconsistency of Ampere's Law and Displacement Current Density, Maxwell's Equations in Different Final Forms and Word Statements. Conditions at a Boundary Surface : Dielectric-Dielectric and Dielectric-Conductor Interfaces. Illustrative Problems. [1,2]

**UNIT III: EM Wave Characteristics - I:** Wave Equations for Conducting and Perfect Dielectric Media, Uniform Plane Waves – Definition, All Relations Between E & H, Sinusoidal Variations, Wave Propagation in Lossy dielectrics, lossless dielectrics, free space, wave propagation in good conductors, skin depth, Polarization & Types. Illustrative Problems. [1,2,3]

**UNIT IV: EM Wave Characteristics – II:** Reflection and Refraction of Plane Waves – Normal and Oblique Incidences, for both Perfect Conductor and Perfect Dielectrics, Brewster Angle, Critical Angle and Total Internal Reflection, Surface Impedance. Poynting Vector and Poynting Theorem – Applications, Power Loss in a Plane Conductor. Illustrative Problems. [2,3,4]

**UNIT V: Transmission Lines - I :** Types, Parameters, T& $\pi$  Equivalent Circuits, Transmission Line Equations, Primary & Secondary Constants, Expressions for Characteristic Impedance, Propagation Constant, Phase and Group Velocities, Infinite Line, Lossless lines, distortion less lines, Loading - Types of Loading. Illustrative Problems. [1,7]

**UNIT VI: Transmission Lines – II :** Input Impedance Relations, SC and OC Lines, Reflection Coefficient, VSWR. Low loss radio frequency lines and UHF Transmission lines, UHF Lines as Circuit Elements; Impedance Transformations  $\lambda/4$ ,  $\lambda/2$ ,  $\lambda/8$  Lines –. Smith Chart – Construction and Applications, Quarter wave transformer, Stub Matching-single & double, Illustrative Problems. [1,7]

### **TEXT BOOKS:**

1. Elements of Electromagnetic – Matthew N.O. Sadiku, Oxford Univ. Press, 3rd ed., 2001.
2. Electromagnetic Waves and Radiating Systems – E.C. Jordan and K.G. Balmain, PHI, 2<sup>nd</sup> Edition, 2000.

### **REFERENCE BOOKS:**

1. Electromagnetic Fields and Wave Theory –GSN Raju, Pearson Education 2006
2. Engineering Electromagnetics:Nathan Ida, Springer(India)Pvt.Ltd., New Delhi, 2nd ed., 2005.
3. Engineering Electromagnetics – William H. Hayt Jr. and John A. Buck, TMH, 7th ed., 2006.
4. Electromagnetic Field Theory and Transmission Lines: G SasiBhushana Rao,Wiley India 2013
5. Transmission Lines and Networks–Umesh Sinha,Satya Prakashan (Tech. India Publications), New Delhi, 2001.
6. Electromagnetic waves and transmission lines – R S Rao, PHI, EEE edition

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### **OUTCOMES:**

#### **At the end of this course the student can able to:**

1. Determine E and H using various laws and applications of electric & magnetic fields
2. Apply the Maxwell equations to analyze the time varying behavior of EM waves
3. Gain the knowledge in uniform plane wave concept and characteristics of uniform plane wave in various media
4. Calculate Brewster angle, critical angle and total internal reflection
5. Derive the expressions for input impedance of transmission lines
6. Calculate reflection coefficient, VSWR etc. using smith chart

II Year - II Semester

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## ANALOG COMMUNICATIONS

### UNIT I

**AMPLITUDE MODULATION** : Introduction to communication system, Need for modulation, Frequency Division Multiplexing , Amplitude Modulation, Definition, Time domain and frequency domain description, single tone modulation, power relations in AM waves, Generation of AM waves, square law Modulator, Switching modulator, Detection of AM Waves; Square law detector, Envelope detector.

### UNIT II

**DSB & SSB MODULATION** : Double side band suppressed carrier modulators, time domain and frequency domain description, Generation of DSBSC Waves, Balanced Modulators, Ring Modulator, Coherent detection of DSB-SC Modulated waves, COSTAS Loop. Frequency domain description, Frequency discrimination method for generation of AM SSB Modulated Wave, Time domain description, Phase discrimination method for generating AM SSB Modulated waves. Demodulation of SSB Waves, Vestigial side band modulation: Frequency description, Generation of VSB Modulated wave, Time domain description, Envelope detection of a VSB Wave pulse Carrier, Comparison of AM Techniques, Applications of different AM Systems.

### UNIT III

**ANGLE MODULATION** : Basic concepts, Frequency Modulation: Single tone frequency modulation, Spectrum Analysis of Sinusoidal FM Wave, Narrow band FM, Wide band FM, Constant Average Power, Transmission bandwidth of FM Wave - Generation of FM Waves, Direct FM, Detection of FM Waves: Balanced Frequency discriminator, Zero crossing detector, Phase locked loop, Comparison of FM & AM.

### UNIT IV

**TRANSMITTERS & RECEIVERS: Radio Transmitter** - Classification of Transmitter, AM Transmitter, Effect of feedback on performance of AM Transmitter, FM Transmitter – Variable reactance type and phase modulated FM Transmitter, frequency stability in FM Transmitter. **Radio Receiver** - Receiver Types - Tuned radio frequency receiver, Superhetrodyne receiver, RF section and Characteristics - Frequency changing and tracking, Intermediate frequency, AGC, FM Receiver, Comparison with AM Receiver, Amplitude limiting. Communication Receivers, extensions of superhetrodyne principle and additional circuits.

### UNIT V

**NOISE** : Review of noise and noise sources, noise figure, Noise in Analog communication Systems, Noise in DSB& SSB System, Noise in AM System, Noise in Angle Modulation Systems, Threshold effect in Angle Modulation System, Pre-emphasis & de-emphasis

### UNIT VI

**PULSE MODULATION** : Time Division Multiplexing,, Types of Pulse modulation, PAM (Single polarity, double polarity) PWM: Generation & demodulation of PWM, PPM, Generation and demodulation of PPM, TDM Vs FDM

**TEXT BOOKS:**

1. Principles of Communication Systems – H Taub & D. Schilling, Gautam Sahe, TMH, 2007 3<sup>rd</sup> Edition.
2. Communication Systems – B.P. Lathi, BS Publication, 2006.

**REFERENCES:**

1. Principles of Communication Systems - Simon Haykin, John Wiley, 2<sup>nd</sup> Ed.,.
2. Electronics & Communication System – George Kennedy and Bernard Davis, TMH 2004.
3. Communication Systems– R.P. Singh, SP Sapre, Second Edition TMH, 2007.
4. Fundamentals of Communication Systems - John G. Proakis, Masond, Salehi PEA, 2006.
5. Electronic Communication systems – Tomasi, Pearson.

**Course Objectives:**

Students undergoing this course, are expected to

1. Familiarize with the fundamentals of analog communication systems
2. Familiarize with various techniques for analog modulation and demodulation of signals
3. Distinguish the figure of merits of various analog modulation methods
4. Develop the ability to classify and understand various functional blocks of radio transmitters and receivers
5. Familiarize with basic techniques for generating and demodulating various pulse modulated signals

**Course Outcomes:**

After undergoing the course, students will be able to

1. Differentiate various Analog modulation and demodulation schemes and their spectral characteristics
2. Analyze noise characteristics of various analog modulation methods
3. Analyze various functional blocks of radio transmitters and receivers
4. Design simple analog systems for various modulation techniques.

II Year - II Semester

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## PULSE AND DIGITAL CIRCUITS

### OBJECTIVES

The student will be made

- To understand the concept of wave shaping circuits, Switching Characteristics of diode and transistor.
- To study the design and analysis of various Multivibrators.
- To understand the functioning of different types of time-base Generators.
- To learn the working of logic families & Sampling Gates.

### UNIT I

**LINEAR WAVESHAPING:** High pass, low pass RC circuits, their response for sinusoidal, step, pulse, square, ramp and exponential inputs. RC network as differentiator and integrator; Attenuators, its applications in CRO probe, RL and RLC circuits and their response for step input, Ringing circuit.

### UNIT II

**NON-LINEAR WAVE SHAPING :** Diode clippers, Transistor clippers, clipping at two independent levels, Transfer characteristics of clippers, Emitter coupled clipper; Clamping operation, clamping circuits using diode with different inputs, Clamping circuit theorem, practical clamping circuits, effect of diode characteristics on clamping voltage, Transfer characteristics of clampers.

### UNIT III

**SWITCHING CHARACTERISTICS OF DEVICES :** Diode as a switch, piecewise linear diode characteristics, Design and analysis of Transistor as a switch, Break down voltage consideration of transistor, saturation parameters of Transistor and their variation with temperature, Design of transistor switch, transistor-switching times.

**Bistable Multivibrator:** Analysis And Design of Fixed Bias, Self Bias Bistable Multi Vibrator, Collector Catching Diodes, Commutating Capacitors, Triggering of Binary Circuits, Emitter Coupled Bistable Multivibrator (Schmitt Trigger).

### UNIT IV

**Monostable Multivibrator:** Analysis and Design of Collector Coupled Monostable Multi vibrator, Triggering of Monostable Multivibrator, Applications of Monostable Multivibrator.

**Astable Multivibrator:** Analysis and Design of Collector Coupled Astable Multivibrator, Application of Astable Multivibrator as a Voltage to Frequency Converter.

### UNIT V

#### VOLTAGE TIME BASE GENERATORS:

General features of a time base signal, Methods of generating time base waveform Exponential Sweep Circuits, Negative Resistance Switches, basic principles in Miller and Bootstrap time base generators, Transistor Miller time base generator, Transistor Bootstrap time base generator.

### UNIT VI

#### LOGIC FAMILIES & SAMPLING GATES:

**LOGIC FAMILIES:** Diode Logic, Transistor Logic, Diode-Transistor Logic, Transistor-Transistor Logic, Emitter Coupled Logic, AOI Logic, Comparison of Logic Families.

**SAMPLING GATES:** Basic Operating Principles of Sampling Gates, Diode Unidirectional Sampling Gate and Two-Diode Bi-Directional Sampling Gate, Four-Diode gates, Six-Diode Gates, Reduction of Pedestal in Sampling Gates, Applications of Sampling Gates.



**TEXT BOOKS:**

1. Pulse, Digital and Switching Waveforms - J. Millman and H. Taub, McGraw-Hill
2. Pulse and Digital Circuits – A. Anand Kumar, PHI, 2005

**REFERENCES:**

1. Pulse, Digital and Switching Waveforms - J. Millman and H. Taub, Mothiki S Prakash Rao McGraw-Hill, Second Edition, 2007.
2. Solid State Pulse circuits - David A. Bell, PHI, 4th Edn., 2002
3. Pulse & Digital Circuits by Venkata Rao,K,Ramasudha K, Manmadha Rao,G., Pearson,2010

**OUTCOMES**

After going through this course the student will be able to

- Design linear and non-linear wave shaping circuits.
- Apply the fundamental concepts of wave shaping for various switching and signal generating circuits.
- Design different multivibrators and time base generators.
- Utilize the non sinusoidal signals in many experimental research areas.

II Year - II Semester

L	T	P	C
4	0	0	3

## MANAGEMENT SCIENCE

### Course Objectives:

- \*To familiarize with the process of management and to provide basic insight into select contemporary management practices
- \*To provide conceptual knowledge on functional management and strategic management.

### UNIT I

**Introduction to Management:** Concept –nature and importance of Management –Generic Functions of Management – Evaluation of Management thought- Theories of Motivation – Decision making process-Designing organization structure- Principles of organization – Organizational typology- International Management: Global Leadership and Organizational behavior Effectiveness(GLOBE) structure

### UNIT II

**Operations Management:** Principles and Types of Management – Work study- Statistical Quality Control- Control charts (P-chart, R-chart, and C-chart) Simple problems- Material Management: Need for Inventory control- EOQ, ABC analysis (simple problems) and Types of ABC analysis (HML, SDE, VED, and FSN analysis).

### UNIT III

**Functional Management:** Concept of HRM, HRD and PMIR- Functions of HR Manager- Wage payment plans(Simple Problems) – Job Evaluation and Merit Rating - Marketing Management- Functions of Marketing – Marketing strategies based on product Life Cycle, Channels of distributions. Operationlizing change through performance management.

### UNIT IV

**Project Management:** (PERT/CPM): Development of Network – Difference between PERT and CPM Identifying Critical Path- Probability- Project Crashing (Simple Problems)

### UNIT V

**Strategic Management:** Vision, Mission, Goals, Strategy – Elements of Corporate Planning Process – Environmental Scanning – SWOT analysis- Steps in Strategy Formulation and Implementation, Generic Strategy Alternatives. Global strategies, theories of Multinational Companies.

### UNIT VI

**Contemporary Management Practice:** Basic concepts of MIS, MRP, Justin- Time(JIT) system, Total Quality Management(TQM), Six sigma and Capability Maturity Model(CMM) Levies, Supply Chain Management , Enterprise Resource Planning (ERP), Business Process outsourcing (BPO), Business process Re-engineering and Bench Marking, Balanced Score Card.

### Course Outcome:

- \*After completion of the Course the student will acquire the knowledge on management functions, global leadership and organizational behavior.
- \*Will familiarize with the concepts of functional management project management and strategic management.

### **Text Books**

1. Dr. P. Vijaya Kumar & Dr. N. Appa Rao, '*Management Science*' Cengage, Delhi, 2012.
2. Dr. A. R. Aryasri, '*Management Science*' TMH 2011.

### **References**

1. Koontz & Weihrich: '*Essentials of management*' TMH 2011
2. Seth & Rastogi: *Global Management Systems*, Cengage learning , Delhi, 2011
3. Robbins: *Organizational Behaviour*, Pearson publications, 2011
4. Kanishka Bedi: *Production & Operations Management*, Oxford Publications, 2011
5. Philip Kotler & Armstrong: *Principles of Marketing*, Pearson publications
6. Biswajit Patnaik: *Human Resource Management*, PHI, 2011
7. Hitt and Vijaya Kumar: *Starategic Management*, Cengage learning
8. Prem Chadha: *Performance Management*, Trinity Press(An imprint of Laxmi Publications Pvt. Ltd.) Delhi 2015.
9. Anil Bhat& Arya Kumar : *Principles of Management*, Oxford University Press, New Delhi, 2015.

**II Year - II Semester**

<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>0</b>	<b>0</b>	<b>3</b>	<b>2</b>

### **ELECTRONIC CIRCUIT ANALYSIS LAB**

**Note:** The students are required to design the circuit and perform the simulation using Multisim/ Equivalent Industrial Standard Licensed simulation software tool. Further they are required to verify the result using necessary hardware equipment.

#### **List of Experiments :( Minimum of Ten Experiments has to be performed)**

1. Determination of  $f_T$  of a given transistor.
2. Voltage-Series Feedback Amplifier
3. Current-Shunt Feedback Amplifier
4. RC Phase Shift/Wien Bridge Oscillator
5. Hartley/Colpitt's Oscillator
6. Two Stage RC Coupled Amplifier
7. Darlington Pair Amplifier
8. Bootstrapped Emitter Follower
9. Class A Series-fed Power Amplifier
10. Transformer-coupled Class A Power Amplifier
11. Class B Push-Pull Power Amplifier
12. Complementary Symmetry Class B Push-Pull Power Amplifier
13. Single Tuned Voltage Amplifier
14. Double Tuned Voltage Amplifier

#### **Equipment required:**

#### **Software:**

- i. Multisim/ Equivalent Industrial Standard Licensed simulation software tool.
- ii. Computer Systems with required specifications

#### **Hardware:**

10. Regulated Power supplies
11. Analog/Digital Storage Oscilloscopes
12. Analog/Digital Function Generators
13. Digital Multimeters
14. Decade Résistance Boxes/Rheostats
15. Decade Capacitance Boxes
16. Ammeters (Analog or Digital)
17. Voltmeters (Analog or Digital)
18. Active & Passive Electronic Components

**II Year - II Semester**

<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>0</b>	<b>0</b>	<b>3</b>	<b>2</b>

**ANALOG COMMUNICATIONS LAB**

List of Experiments (Twelve experiments to be done- **The students have to calculate the relevant parameters** ) -

(a. Hardware, b. MATLAB Simulink, c. MATLAB Communication tool box)

- A. Amplitude Modulation - Mod. & Demod.
- B. AM - DSB SC - Mod. & Demod.
- C. Spectrum Analysis of Modulated signal using Spectrum Analyser
- D. Diode Detector
- E. Pre-emphasis & De-emphasis
- F. Frequency Modulation - Mod. & Demod.
- G. AGC Circuits
- H. Sampling Theorem
- I. Pulse Amplitude Modulation - Mod. & Demod.
- J. PWM , PPM - Mod. & Demod.
- K. PLL
- L. Radio receiver characteristics

**Equipments & Software required:**

**Software :**

- i.) Computer Systems with latest specifications
- ii) Connected in Lan (Optional)
- iii) Operating system (Windows XP)
- iv) Simulations software (Simulink & MATLAB)

**Equipment:**

- 1. RPS - 0 – 30 V
- 2. CRO - 0 – 20 M Hz.
- 3. Function Generators - 0 – 1 M Hz
- 4. Components
- 5. Multimeters
- 6. Spectrum Analyser

III Year - I Semester

L	T	P	C
4	0	0	3

## COMPUTER ARCHITECTURE AND ORGANIZATION

### OBJECTIVES:

- Understand the architecture of a modern computer with its various processing units. Also the Performance measurement of the computer system.
- In addition to this the memory management system of computer.

### UNIT -I:

**Basic Structure Of Computers:** Functional unit, Basic Operational concepts, Bus structures, System Software, Performance, The history of computer development.

### UNIT -II:

#### **Machine Instruction and Programs:**

Instruction and Instruction Sequencing: Register Transfer Notation, Assembly Language Notation, Basic Instruction Types,

Addressing Modes, Basic Input/output Operations, The role of Stacks and Queues in computer programming equation. Component of Instructions: Logic Instructions, shift and Rotate Instructions

### UNIT -III:

**Type of Instructions:** Arithmetic and Logic Instructions, Branch Instructions, Addressing Modes, Input/output Operations

### UNIT -IV:

**INPUT/OUTPUT ORGANIZATION:** Accessing I/O Devices, Interrupts: Interrupt Hardware, Enabling and Disabling Interrupts, Handling Multiple Devices, Direct Memory Access,

Buses: Synchronous Bus, Asynchronous Bus, Interface Circuits, Standard I/O Interface: Peripheral Component Interconnect (PCI) Bus, Universal Serial Bus (USB)

### UNIT -V:

**The MEMORY SYSTEMS:** Basic memory circuits, Memory System Consideration, Read-Only Memory: ROM, PROM, EPROM, EEPROM, Flash Memory,

Cache Memories: Mapping Functions, INTERLEAVING

**Secondary Storage:** Magnetic Hard Disks, Optical Disks,

### UNIT -VI:

**Processing Unit:** Fundamental Concepts: Register Transfers, Performing An Arithmetic Or Logic Operation, Fetching A Word From Memory,

Execution of Complete Instruction, Hardwired Control,

**Micro programmed Control:** Microinstructions, Micro program Sequencing, Wide Branch Addressing Microinstructions with next –Address Field

**OUTCOMES:**

- Students can understand the architecture of modern computer.
- They can analyze the Performance of a computer using performance equation
- Understanding of different instruction types.
- 4. Students can calculate the effective address of an operand by addressing modes
- 5. They can understand how computer stores positive and negative numbers.
- 6. Understanding of how a computer performs arithmetic operation of positive and negative numbers.

**TEXT BOOKS:**

1. Computer Organization, Carl Hamacher, Zvonks Vranesic, Safea Zaky, 5th Edition, McGraw Hill.
2. Computer Architecture and Organization , John P. Hayes ,3<sup>rd</sup> Edition, McGraw Hill.

**REFERENCE BOOKS:**

1. Computer Organization and Architecture – William Stallings Sixth Edition, Pearson/PHI
2. Structured Computer Organization – Andrew S. Tanenbaum, 4th Edition PHI/Pearson
3. Fundamentals of Computer Organization and Design, - Sivaraama Dandamudi Springer Int. Edition.
4. “Computer Organization and Design: The Hardware/Software Interface” by David A. Patterson and John L. Hennessy.
5. J .P. Hayes, "Computer Architecture and Organization", McGraw-Hill, 1998.

III Year - I Semester

<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>4</b>	<b>0</b>	<b>0</b>	<b>3</b>

## LINEAR IC APPLICATIONS

### OBJECTIVES

- To understand the basic operation & performance parameters of differential amplifiers.
- To understand & learn the measuring techniques of performance parameters of OP-AMP
- To learn the linear and non-linear applications of operational amplifiers.
- To understand the analysis & design of different types of active filters using opamps
- To learn the internal structure, operation and applications of different analog ICs
- To Acquire skills required for designing and testing integrated circuits

### UNIT I

**INTEGRATED CIRCUITS:** Differential Amplifier- DC and AC analysis of Dual input Balanced output Configuration, Properties of other differential amplifier configuration (Dual Input Unbalanced Output, Single Ended Input – Balanced/ Unbalanced Output), DC Coupling and Cascade Differential Amplifier Stages, Level translator.

### UNIT II

Characteristics of OP-Amps, Integrated circuits-Types, Classification, Package Types and Temperature ranges, Power supplies, Op-amp Block Diagram, ideal and practical Op-amp Specifications, DC and AC characteristics, 741 op-amp & its features, Op-Amp parameters & Measurement, Input & Out put Off set voltages & currents, slew rate, CMRR, PSRR, drift, Frequency Compensation techniques.

### UNIT III

**LINEAR and NON-LINEAR APPLICATIONS OF OP-AMPS:** Inverting and Non-inverting amplifier, Integrator and differentiator, Difference amplifier, Instrumentation amplifier, AC amplifier, V to I, I to V converters, Buffers. Non- Linear function generation, Comparators, Multivibrators, Triangular and Square wave generators, Log and Anti log Amplifiers, Precision rectifiers.

### UNIT IV

**ACTIVE FILTERS, ANALOG MULTIPLIERS AND MODULATORS:** Design & Analysis of Butterworth active filters – 1st order, 2nd order LPF, HPF filters. Band pass, Band reject and all pass filters. Four Quadrant Multiplier, IC 1496, Sample & Hold circuits.

### UNIT V

**TIMERS & PHASE LOCKED LOOPS:** Introduction to 555 timer, functional diagram, Monostable and Astable operations and applications, Schmitt Trigger; PLL - introduction, block schematic, principles and description of individual blocks, 565 PLL, Applications of PLL – frequency multiplication, frequency translation, AM, FM & FSK demodulators. Applications of VCO (566).

### UNIT VI

**DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVERTERS:** Introduction, basic DAC techniques, weighted resistor DAC, R-2R ladder DAC, inverted R-2R DAC, and IC 1408 DAC, Different types of ADCs – parallel Comparator type ADC, counter type ADC, successive approximation ADC and dual slope ADC. DAC and ADC Specifications, Specifications AD 574 (12 bit ADC).



**TEXT BOOKS:**

1. Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003.
2. Op-Amps & Linear ICs - Ramakanth A. Gayakwad, PHI,1987.
3. Operational Amplifiers–C.G. Clayton, Butterworth & Company Publ. Ltd./Elsevier, 1971

**REFERENCES :**

1. Operational Amplifiers & Linear Integrated Circuits –Sanjay Sharma ;SK Kataria &Sons;2<sup>nd</sup> Edition,2010
2. Design with Operational Amplifiers & Analog Integrated Circuits – Sergio Franco, McGraw Hill, 1988.
3. OP AMPS and Linear Integrated Circuits concepts and Applications, James M Fiore, Cenage Learning India Ltd.
4. Operational Amplifiers & Linear Integrated Circuits–R.F.Coughlin & Fredrick Driscoll, PHI, 6th Edition.
5. Operational Amplifiers & Linear ICs – David A Bell, Oxford Uni. Press, 3rd Edition

**OUTCOMES**

- Design circuits using operational amplifiers for various applications.
- Analyze and design amplifiers and active filters using Op-amp.
- Diagnose and trouble-shoot linear electronic circuits.
- Understand the gain-bandwidth concept and frequency response of the amplifier configurations.
- Understand thoroughly the operational amplifiers with linear integrated circuits.

III Year - I Semester

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## DIGITAL IC APPLICATIONS

### OBJECTIVES

The main objectives of this course are:

- Introduction of digital logic families and interfacing concepts for digital design is considered.
- VHDL fundamentals were discussed to modeling the digital system design blocks.
- VHDL compilers, simulators and synthesis tools are described, which are used to verify digital systems in a technology-independent fashion.
- Design and implementation of combinational and sequential digital logic circuits is explained.

### Outcomes:

At the end of this course the student can able to:

- Understand the structure of commercially available digital integrated circuit families.
- Learn the IEEE Standard 1076 Hardware Description Language (VHDL).
- Model complex digital systems at several levels of abstractions, behavioral, structural, simulation, synthesis and rapid system prototyping.
- Analyze and design basic digital circuits with combinatorial and sequential logic circuits using VHDL.

### Syllabus:

#### UNIT-I

**Digital Logic Families and Interfacing:** Introduction to logic families, CMOS logic, CMOS steady state and dynamic electrical behavior, CMOS logic families. Bipolar logic, transistor-transistor logic, TTL families, CMOS/TTL interfacing, low voltage CMOS logic and interfacing, Emitter coupled logic.

(Text book-1)

#### UNIT-II

**Introduction to VHDL:** Design flow, program structure, levels of abstraction, Elements of VHDL: Data types, data objects, operators and identifiers. Packages, Libraries and Bindings, Subprograms. VHDL Programming using structural and data flow modeling.

(Text book-2)

#### UNIT-III

**Behavioral Modeling:** Process statement, variable assignment statement, signal assignment statement, wait statement, if statement, case statement, null statement, loop statement, exit statement, next statement, assertion statement, more on signal assignment statement, Inertial Delay Model, Transport Delay Model, Creating Signal Waveforms, Signal Drivers, Other Sequential Statements, Multiple Processes. Logic Synthesis, Inside a logic Synthesizer.

(Text book-2)

#### **UNIT-IV**

**Combinational Logic Design:** Binary Adder-Subtractor, Ripple Adder, Look Ahead Carry Generator, ALU, Decoders, encoders, multiplexers and demultiplexers, parity circuits, comparators, Barrel Shifter, Simple Floating-Point Encoder, Dual Priority Encoder, Design considerations of the above combinational logic circuits with relevant Digital ICs, modeling of above ICs using VHDL.

**(Text book-1)**

#### **UNIT-V**

**Sequential Logic Design:** SSI Latches and flip flops, Ring Counter, Johnson Counter, Design of Modulus N Synchronous Counters, Shift Registers, Universal Shift Registers, Design considerations of the above sequential logic circuits with relevant Digital ICs, modeling of above ICs using VHDL.

**(Text book-1)**

#### **UNIT-VI:**

**Synchronous and Asynchronous Sequential Circuits:** Basic design steps: State diagram, state table, state assignment, choice of flip flops and derivation of next state and output expressions, timing diagram. State assignment problem: One hot encoding. Mealy and Moore type FSM for serial adder, VHDL code for the serial adder. Analysis of Asynchronous circuits, State Reduction, State Assignment. A complete design example: The vending machine controller.

**(Reference text book- 1)**

#### **Text Books:**

1. Digital Design Principles & Practices – John F. Wakerly, PHI/ Pearson Education Asia, 3rd Ed., 2005.
2. VHDL Primer – J. Bhasker, Pearson Education/ PHI, 3rd Edition.

#### **References:**

1. Fundamentals of Digital Logic with VHDL Design- Stephen Brown, Zvonko Vranesic, McGrawHill, 3<sup>rd</sup> Edition.

III Year - I Semester

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## DIGITAL COMMUNICATIONS

### UNIT I

**PULSE DIGITAL MODULATION:** Elements of digital communication systems, advantages of digital communication systems, Elements of PCM: Sampling, Quantization & Coding, Quantization error, Companding in PCM systems. Differential PCM systems (DPCM). Delta modulation, its draw backs, adaptive delta modulation, comparison of PCM and DM systems, noise in PCM and DM systems.

### UNIT II

**DIGITAL MODULATION TECHNIQUES:** Introduction, ASK, FSK, PSK, DPSK, DEPSK, QPSK, M-ary PSK, ASK, FSK, similarity of BFSK and BPSK.

### UNIT III

**DATA TRANSMISSION :** Base band signal receiver, probability of error, the optimum filter, matched filter, probability of error using matched filter, coherent reception, non-coherent detection of FSK, calculation of error probability of ASK, BPSK, BFSK, QPSK.

### UNIT IV

**INFORMATION THEORY:** Discrete messages, concept of amount of information and its properties. Average information, Entropy and its properties. Information rate, Mutual information and its properties.

### UNIT V

**SOURCE CODING:** Introductions, Advantages, Shannon's theorem, Shannon-Fano coding, Huffman coding, efficiency calculations, channel capacity of discrete and analog Channels, capacity of a Gaussian channel, bandwidth –S/N trade off.

### UNIT VI

**LINEAR BLOCK CODES:** Introduction, Matrix description of Linear Block codes, Error detection and error correction capabilities of Linear block codes, Hamming codes, Binary cyclic codes, Algebraic structure, encoding, syndrome calculation, BCH Codes.

**CONVOLUTION CODES:** Introduction, encoding of convolution codes, time domain approach, transform domain approach. Graphical approach: state, tree and trellis diagram decoding using Viterbi algorithm.

### TEXT BOOKS:

1. Digital communications - Simon Haykin, John Wiley, 2005
2. Principles of Communication Systems – H. Taub and D. Schilling, TMH, 2003

### REFERENCES:

1. Digital and Analog Communication Systems - Sam Shanmugam, John Wiley, 2005.
2. Digital Communications – John Proakis, TMH, 1983. Communication Systems Analog & Digital – Singh & Sapre, TMH, 2004.
3. Modern Analog and Digital Communication – B.P.Lathi, Oxford reprint, 3rd edition, 2004.

Students undergoing this course are expected to:

**Course Objectives:**

1. Understand different pulse digital modulation techniques and their comparison
2. Familiarize various digital modulation techniques and calculation of their error probabilities
3. Understand the concept of entropy and different source coding techniques
4. Familiarize with block codes, cyclic codes and convolutional codes

**Course Outcomes:**

After undergoing the course students will be able to:

1. Determine the performance of different waveform coding techniques for the generation and digital representation of the signals.
2. Determine the probability of error for various digital modulation schemes
3. Analyze different source coding techniques
4. Compute and analyze different error control coding schemes for the reliable transmission of digital information over the channel.

III Year - I Semester

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## ANTENNA AND WAVE PROPAGATION

### OBJECTIVES

The student will be able to

- understand the applications of the electromagnetic waves in free space.
- introduce the working principles of various types of antennas
- discuss the major applications of antennas with an emphasis on how antennas are employed to meet electronic system requirements.
- understand the concepts of radio wave propagation in the atmosphere.

### UNIT I

**ANTENNA FUNDAMENTALS:** Introduction, Radiation Mechanism – single wire, 2 wire, dipoles, Current Distribution on a thin wire antenna. Antenna Parameters - Radiation Patterns, Patterns in Principal Planes, Main Lobe and Side Lobes, Beamwidths, Polarization, Beam Area, Radiation Intensity, Beam Efficiency, Directivity, Gain and Resolution, Antenna Apertures, Aperture Efficiency, Effective Height, illustrated Problems.

### UNIT II

**THIN LINEAR WIRE ANTENNAS:** Retarded Potentials, Radiation from Small Electric Dipole, Quarter wave Monopole and Half wave Dipole – Current Distributions, Evaluation of Field Components, Power Radiated, Radiation Resistance, Beamwidths, Directivity, Effective Area and Effective Height. Natural current distributions, fields and patterns of Thin Linear Center-fed Antennas of different lengths, Radiation Resistance at a point which is not current maximum. Antenna Theorems – Applicability and Proofs for equivalence of directional characteristics, Loop Antennas: Small Loops - Field Components, Comparison of far fields of small loop and short dipole, Concept of short magnetic dipole,  $D$  and  $R_r$  relations for small loops.

### UNIT III

**ANTENNA ARRAYS :** 2 element arrays – different cases, Principle of Pattern Multiplication,  $N$  element Uniform Linear Arrays – Broadside, End-fire Arrays, EFA with Increased Directivity, Derivation of their characteristics and comparison; Concept of Scanning Arrays. Directivity Relations (no derivations). Related Problems. Binomial Arrays, Effects of Uniform and Non-uniform Amplitude Distributions, Design Relations. Arrays with Parasitic Elements, Yagi-Uda Arrays, Folded Dipoles and their characteristics.

### UNIT IV

**NON-RESONANT RADIATORS :** Introduction, Traveling wave radiators – basic concepts, Long wire antennas – field strength calculations and patterns, Microstrip Antennas-Introduction, Features, Advantages and Limitations, Rectangular Patch Antennas –Geometry and Parameters, Impact of different parameters on characteristics. Broadband Antennas: Helical Antennas – Significance, Geometry, basic properties; Design considerations for monofilar helical antennas in Axial Mode and Normal Modes (Qualitative Treatment).

## UNIT V

**VHF, UHF AND MICROWAVE ANTENNAS :** Reflector Antennas : Flat Sheet and Corner Reflectors. Paraboloidal Reflectors – Geometry, characteristics, types of feeds, F/D Ratio, Spill Over, Back Lobes, Aperture Blocking, Off-set Feeds, Cassegrain Feeds.

Horn Antennas – Types, Optimum Horns, Design Characteristics of Pyramidal Horns; Lens Antennas – Geometry, Features, Dielectric Lenses and Zoning, Applications, Antenna Measurements – Patterns Required, Set Up, Distance Criterion, Directivity and Gain Measurements (Comparison, Absolute and 3-Antenna Methods).

## UNIT VI

**WAVE PROPAGATION :** Concepts of Propagation – frequency ranges and types of propagations. Ground Wave Propagation–Characteristics, Parameters, Wave Tilt, Flat and Spherical Earth Considerations. Sky Wave Propagation – Formation of Ionospheric Layers and their Characteristics, Mechanism of Reflection and Refraction, Critical Frequency, MUF and Skip Distance – Calculations for flat and spherical earth cases, Optimum Frequency, LUHF, Virtual Height, Ionospheric Abnormalities, Ionospheric Absorption.

Fundamental Equation for Free-Space Propagation, Basic Transmission Loss Calculations. Space Wave Propagation – Mechanism, LOS and Radio Horizon. Tropospheric Wave Propagation – Radius of Curvature of path, Effective Earth's Radius, Effect of Earth's Curvature, Field Strength Calculations, M-curves and Duct Propagation, Tropospheric Scattering.

## TEXT BOOKS

1. Antennas for All Applications – John D. Kraus and Ronald J. Marhefka, 3<sup>rd</sup> Edition, TMH, 2003.
2. Electromagnetic Waves and Radiating Systems – E.C. Jordan and K.G. Balmain, PHI, 2<sup>nd</sup> Edition, 2000.

## REFERENCES

1. Antenna Theory - C.A. Balanis, John Wiley and Sons, 2<sup>nd</sup> Edition, 2001.
2. Antennas and Wave Propagation – K.D. Prasad, Satya Prakashan, Tech India Publications, New Delhi, 2001.
3. Transmission and Propagation – E.V.D. Glazier and H.R.L. Lamont, The Services Text Book of Radio, vol. 5, Standard Publishers Distributors, Delhi.
4. Electronic and Radio Engineering – F.E. Terman, McGraw-Hill, 4<sup>th</sup> Edition, 1955.
5. Antennas – John D. Kraus, McGraw-Hill, 2<sup>nd</sup> Edition, 1988.

## OUTCOMES

After going through this course the student will be able to

- Identify basic antenna parameters.
- Design and analyze wire antennas, loop antennas, reflector antennas, lens antennas, horn antennas and microstrip antennas
- Quantify the fields radiated by various types of antennas
- Design and analyze antenna arrays
- Analyze antenna measurements to assess antenna's performance
- Identify the characteristics of radio wave propagation

III Year - I Semester

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**PULSE & DIGITAL CIRCUITS LAB**

- 1. Linear wave shaping.**
- 2. Non Linear wave shaping – Clippers.**
- 3. Non Linear wave shaping – Clampers.**
- 4. Transistor as a switch.**
- 5. Study of Logic Gates & Some applications.**
- 6. Study of Flip-Flops & some applications.**
- 7. Sampling Gates.**
- 8. Astable Multivibrator.**
- 9. Monostable Multivibrator.**
- 10. Bistable Multivibrator.**
- 11. Schmitt Trigger.**
- 12. UJT Relaxation Oscillator.**
- 13. Bootstrap sweep circuit.**

**Equipment required for Laboratory:**

- 1. RPS - 0 – 30 V**
- 2. CRO - 0 – 20 M Hz.**
- 3. Function Generators - 0 – 1 M Hz**
- 4. Components**
- 5. Multi Meters**



III Year - I Semester

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**L IC APPLICATIONS LAB**

**Minimum Twelve Experiments to be conducted :**

1. Study of OP AMPs – IC 741, IC 555, IC 565, IC 566, IC 1496 – functioning, parameters and Specifications.
2. OP AMP Applications – Adder, Subtractor, Comparator Circuits.
3. Integrator and Differentiator Circuits using IC 741.
4. Active Filter Applications – LPF, HPF (first order)
5. Active Filter Applications – BPF, Band Reject (Wideband) and Notch Filters.
6. IC 741 Oscillator Circuits – Phase Shift and Wien Bridge Oscillators.
7. Function Generator using OP AMPs.
8. IC 555 Timer – Monostable Operation Circuit.
9. IC 555 Timer – Astable Operation Circuit.
10. Schmitt Trigger Circuits – using IC 741 and IC 555.
11. IC 565 – PLL Applications.
12. IC 566 – VCO Applications.
13. Voltage Regulator using IC 723.
14. Three Terminal Voltage Regulators – 7805, 7809, 7912.

**Equipment required for Laboratories:**

1. RPS
2. CRO
3. Function Generator
4. Multi Meters
5. IC Trainer Kits (Optional)
6. Bread Boards
7. Components:- IC741, IC555, IC565, IC1496, IC723, 7805, 7809, 7912 and other essential components.
8. Analog IC Tester

III Year - I Semester

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### DICA LABORATORY

**Note:** The students are required to design and draw the internal logical structure of the following Digital Integrated Circuits and to develop VHDL/Verilog HDL Source code, perform simulation using relevant simulator and analyze the obtained simulation results using necessary synthesizer.

All the experiments are required to verify and implement the logical operations on the latest FPGA Hardware in the Laboratory.

#### List of Experiments :( Minimum of Ten Experiments has to be performed)

1. Realization of Logic Gates
2. Design of Full Adder using 3 modeling systems
3. 3 to 8 Decoder -74138
4. 8 to 3 Encoder (with and without parity)
5. 8 x 1 Multiplexer-74151 and 2x 4 De-multiplexer-74155
6. 4- Bit comparator-7485
7. D Flip-Flop-7474
8. Decade counter -7490
9. Shift registers-7495
10. 8-bit serial in-parallel out and parallel in-serial out
11. Fast In & Fast Out (FIFO)
12. MAC ( Multiplier & Accumulator)
13. ALU Design.

#### Equipment/Software required:

1. Xilinx Vivado software / Equivalent Industry Standard Software
2. Xilinx Hardware / Equivalent hardware
3. Personal computer system with necessary software to run the programs and Implement.

III Year - I Semester

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## PROFESSIONAL ETHICS AND HUMAN VALUES

### Course Objectives:

**\*To give basic insights and inputs to the student to inculcate Human values to grow as a responsible human beings with proper personality.**

**\*Professional Ethics instills the student to maintain ethical conduct and discharge their professional duties.**

### UNIT I: Human Values:

Morals, Values and Ethics – Integrity – Trustworthiness - Work Ethics – Service Learning – Civic Virtue – Respect for others – Living Peacefully – Caring – Sharing – Honesty – Courage – Value Time – Co-operation – Commitment – Empathy – Self-confidence – Spirituality- Character.

### UNIT II: Principles for Harmony:

Truthfulness – Customs and Traditions - Value Education – Human Dignity – Human Rights – Fundamental Duties - Aspirations and Harmony (I, We & Nature) – Gender Bias - Emotional Intelligence – Salovey – Mayer Model – Emotional Competencies – Conscientiousness.

### UNIT III: Engineering Ethics and Social Experimentation:

History of Ethics - Need of Engineering Ethics - Senses of Engineering Ethics- Profession and Professionalism — Self Interest - Moral Autonomy – Utilitarianism – Virtue Theory - Uses of Ethical Theories - Deontology- Types of Inquiry – Kohlberg’s Theory - Gilligan’s Argument – Heinz’s Dilemma - Comparison with Standard Experiments — Learning from the Past – Engineers as Managers – Consultants and Leaders – Balanced Outlook on Law - Role of Codes – Codes and Experimental Nature of Engineering.

### UNIT IV: Engineers’ Responsibilities towards Safety and Risk:

Concept of Safety - Safety and Risk – Types of Risks – Voluntary v/s Involuntary Risk – Consequences - Risk Assessment – Accountability – Liability - Reversible Effects - Threshold Levels of Risk - Delayed v/s Immediate Risk - Safety and the Engineer – Designing for Safety – Risk-Benefit Analysis-Accidents.

### UNIT V: Engineers’ Duties and Rights:

Concept of Duty - Professional Duties – Collegiality - Techniques for Achieving Collegiality – Senses of Loyalty - Consensus and Controversy - Professional and Individual Rights – Confidential and Proprietary Information - Conflict of Interest-Ethical egoism - Collective Bargaining – Confidentiality - Gifts and Bribes - Problem solving- Occupational Crimes- Industrial Espionage- Price Fixing-Whistle Blowing.

### UNIT VI: Global Issues:

Globalization and MNCs – Cross Culture Issues - Business Ethics – Media Ethics - Environmental Ethics – Endangering Lives - Bio Ethics - Computer Ethics - War Ethics – Research Ethics - Intellectual Property Rights.

- Related Cases Shall be dealt where ever necessary.

**Outcome:**

**\*It gives a comprehensive understanding of a variety issues that are encountered by every professional in discharging professional duties.**

**\*It provides the student the sensitivity and global outlook in the contemporary world to fulfill the professional obligations effectively.**

**References:**

1. Professional Ethics by R. Subramaniam – Oxford Publications, New Delhi.
2. Ethics in Engineering by Mike W. Martin and Roland Schinzinger - Tata McGraw-Hill – 2003.
3. Professional Ethics and Morals by Prof.A.R.Aryasri, DharanikotaSuyodhana - Maruthi Publications.
4. Engineering Ethics by Harris, Pritchard and Rabins, Cengage Learning, New Delhi.
5. Human Values & Professional Ethics by S. B. Gogate, Vikas Publishing House Pvt. Ltd., Noida.
6. Engineering Ethics & Human Values by M.Govindarajan, S.Natarajan and V.S.SenthilKumar-PHI Learning Pvt. Ltd – 2009.
7. Professional Ethics and Human Values by A. Alavudeen, R.Kalil Rahman and M. Jayakumaran – University Science Press.
8. Professional Ethics and Human Values by Prof.D.R.Kiran-Tata McGraw-Hill - 2013
9. Human Values And Professional Ethics by Jayshree Suresh and B. S. Raghavan, S.Chand Publications

III Year - II Semester

L	T	P	C
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## MICROPROCESSORS AND MICROCONTROLLERS

### UNIT-I:

**8086 ARCHITECTURE:** Main features, pin diagram/description, 8086 microprocessor family, 8086 internal architecture, bus interfacing unit, execution unit, interrupts and interrupt responses, 8086 system timing, minimum mode and maximum mode configuration.

### UNIT-II:

**8086 PROGRAMMING:** Program development steps, instructions, addressing modes, assembler directives, writing simple programs with an assembler, assembly language program development tools.

### UNIT-III:

**8086 INTERFACING :** Semiconductor memories interfacing (RAM,ROM), 8254 software programmable timer/counter, Intel 8259 programmable interrupt controller, software and hardware interrupt applications, Intel 8237a DMA controller, Intel 8255 programmable peripheral interface, keyboard interfacing, alphanumeric displays (LED,7-segment display, multiplexed 7-segment display, LCD), Intel 8279 programmable keyboard/display controller, stepper motor, A/D and D/A converters.

### UNIT-IV:

**80386 AND 80486 MICROPROCESSORS:** Introduction, programming concepts, special purpose registers, memory organization, moving to protected mode, virtual mode, memory paging mechanism, architectural differences between 80386 and 80486 microprocessors.

### UNIT-V:

**Intel 8051 MICROCONTROLLER:** Architecture, hardware concepts, input/output ports and circuits, external memory, counters/timers, serial data input/output, interrupts.

Assembly language programming: Instructions, addressing modes, simple programs.

Interfacing: keyboard, displays (LED, 7-segment display unit), A/D and D/A converters.

### UNIT-VI:

**PIC MICROCONTROLLER:** Introduction, characteristics of PIC microcontroller, PIC microcontroller families, memory organization, parallel and serial input and output, timers, Interrupts, PIC 16F877 architecture, instruction set of the PIC 16F877.

### Text Books:

1. Microprocessors and Interfacing – Programming and Hardware by Douglas V Hall, SSSP Rao, Tata McGraw Hill Education Private Limited, 3<sup>rd</sup> Edition.
2. The 8051 Microcontroller & Embedded Systems Using Assembly and C by Kenneth J.Ayala, Dhananjay V.Gadre,Cengage Learning , India Edition.

### References:

1. The Intel Microprocessors-Architecture, Programming, and Interfacing by Barry B.Brey, Pearson, Eighth Edition-2012.
2. Microprocessors and Microcontrollers-Architecture, Programming and System Design by Krishna Kant, PHI Learning Private Limited, Second Edition, 2014.
3. Microprocessors and Microcontrollers by N.Senthil Kumar, M.Saravanan and S.Jeevananthan, Oxford University Press, Seventh Impression 2013

III Year - II Semester

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<b>4</b>	<b>0</b>	<b>0</b>	<b>3</b>

## MICROWAVE ENGINEERING

### OBJECTIVES

The student will

- Understand fundamental characteristics of waveguides and Microstrip lines through electromagnetic field analysis.
- Understand the basic properties of waveguide components and Ferrite materials composition
- Understand the function, design, and integration of the major microwave components oscillators, power amplifier.
- Understand a Microwave test bench setup for measurements.

### UNIT I

**MICROWAVE TRANSMISSION LINES:** Introduction, Microwave Spectrum and Bands, Applications of Microwaves. Rectangular Waveguides – TE/TM mode analysis, Expressions for Fields, Characteristic Equation and Cut-off Frequencies, Filter Characteristics, Dominant and Degenerate Modes, Sketches of TE and TM mode fields in the cross-section, Mode Characteristics – Phase and Group Velocities, Wavelengths and Impedance Relations; Power Transmission and Power Losses in Rectangular Guide, Impossibility of TEM mode. Related Problems.

### UNIT II

**CIRCULAR WAVEGUIDES:** Introduction, Nature of Fields, Characteristic Equation, Dominant and Degenerate Modes.

Cavity Resonators– Introduction, Rectangular and Cylindrical Cavities, Dominant Modes and Resonant Frequencies, Q factor and Coupling Coefficients, Excitation techniques- waveguides and cavities, Related Problems.

**MICROSTRIP LINES**– Introduction, Zo Relations, Effective Dielectric Constant, Losses, Q factor.

### UNIT III

**MICROWAVE TUBES** :Limitations and Losses of conventional tubes at microwave frequencies. Re-entrant Cavities, Microwave tubes – O type and M type classifications. O-type tubes :2 Cavity Klystrons – Structure, Velocity Modulation Process and Applegate Diagram, Bunching Process and Small Signal Theory –Expressions for o/p Power and Efficiency, Applications, Reflex Klystrons – Structure, Applegate Diagram and Principle of working, Mathematical Theory of Bunching, Power Output, Efficiency, Electronic Admittance; Oscillating Modes and o/p Characteristics, Electronic and Mechanical Tuning, Applications, Related Problems.

### UNIT - IV

**HELIX TWTS:** Significance, Types and Characteristics of Slow Wave Structures; Structure of TWT and Suppression of Oscillations, Nature of the four Propagation Constants(Qualitative treatment).

#### M-type Tubes

Introduction, Cross-field effects, Magnetrons – Different Types, 8-Cavity Cylindrical Travelling Wave Magnetron – Hull Cut-off Condition, Modes of Resonance and PI-Mode Operation, Separation of PI-Mode, o/p characteristics.

## UNIT V

**WAVEGUIDE COMPONENTS AND APPLICATIONS - I** :Coupling Mechanisms – Probe, Loop, Aperture types. Waveguide Discontinuities – Waveguide irises, Tuning Screws and Posts, Matched Loads. Waveguide Attenuators – Resistive Card, Rotary Vane types; Waveguide Phase Shifters – Dielectric, Rotary Vane types. Scattering Matrix– Significance, Formulation and Properties. S-Matrix Calculations for – 2 port Junction, E-plane and H-plane Tees, Magic Tee, Hybrid Ring; Directional Couplers – 2Hole, Bethe Hole types, Ferrite Components– Faraday Rotation, S-Matrix Calculations for Gyrator, Isolator, Circulator, Related Problems.

## UNIT VI

**MICROWAVE SOLID STATE DEVICES:** Introduction, Classification, Applications. TEDs – Introduction, Gunn Diode – Principle, RWH Theory, Characteristics, Basic Modes of Operation, Oscillation Modes. Avalanche Transit Time Devices – Introduction, IMPATT and TRAPATT Diodes – Principle of Operation and Characteristics. **MICROWAVE MEASUREMENTS:** Description of Microwave Bench – Different Blocks and their Features, Precautions; Microwave Power Measurement – Bolometer Method. Measurement of Attenuation, Frequency, Q-factor, Phase shift, VSWR, Impedance Measurement.

### TEXT BOOKS:

1. Microwave Devices and Circuits – Samuel Y. Liao, PHI, 3rd Edition, 1994.
2. Foundations for Microwave Engineering – R.E. Collin, IEEE Press, John Wiley, 2nd Edition, 2002.

### REFERENCES:

1. Microwave Principles – Herbert J. Reich, J.G. Skalnik, P.F. Ordung and H.L. Krauss, CBS Publishers and Distributors, New Delhi, 2004
2. Microwave Engineering- Annapurna Das and Sisir K.Das, Mc Graw Hill Education, 3<sup>rd</sup> Edition.
3. Microwave and Radar Engineering-M.Kulkarni, Umesh Publications, 3<sup>rd</sup> Edition.
4. Microwave Engineering – G S N Raju , I K International
5. Microwave and Radar Engineering – G Sasibhushana Rao Pearson

**OUTCOMES** : After going through this course the student will be able to

- Design different modes in waveguide structures
- Calculate S-matrix for various waveguide components and splitting the microwave energy in a desired direction
- Distinguish between Microwave tubes and Solid State Devices, calculation of efficiency of devices.
- Measure various microwave parameters using a Microwave test bench

III Year - II Semester

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## VLSI DESIGN

### Objectives:

#### The main objectives of this course are:

- Basic characteristics of MOS transistor and examines various possibilities for configuring inverter circuits and aspects of latch-up are considered.
- Design processes are aided by simple concepts such as stick and symbolic diagrams but the key element is a set of design rules, which are explained clearly.
- Basic circuit concepts are introduced for MOS processes we can set out approximate circuit parameters which greatly ease the design process.

### Outcomes:

#### At the end of this course the student can able to:

- Understand the properties of MOS active devices and simple circuits configured when using them and the reason for such encumbrances as ratio rules by which circuits can be interconnected in silicon.
- Know three sets of design rules with which nMOS and CMOS designs may be fabricated.
- Understand the scaling factors determining the characteristics and performance of MOS circuits in silicon.

### Syllabus:

#### UNIT-I:

**Introduction and Basic Electrical Properties of MOS Circuits:** Introduction to IC technology, Fabrication process: nMOS, pMOS and CMOS.  $I_{ds}$  versus  $V_{ds}$  Relationships, Aspects of MOS transistor Threshold Voltage, MOS transistor Trans, Output Conductance and Figure of Merit. nMOS Inverter, Pull-up to Pull-down Ratio for nMOS inverter driven by another nMOS inverter, and through one or more pass transistors. Alternative forms of pull-up, The CMOS Inverter, Latch-up in CMOS circuits, Bi-CMOS Inverter, Comparison between CMOS and BiCMOS technology.

(Text Book-1)

#### UNIT-II:

**MOS and Bi-CMOS Circuit Design Processes:** MOS Layers, Stick Diagrams, Design Rules and Layout, General observations on the Design rules,  $2\mu\text{m}$  Double Metal, Double Poly, CMOS/BiCMOS rules,  $1.2\mu\text{m}$  Double Metal, Double Poly CMOS rules, Layout Diagrams of NAND and NOR gates and CMOS inverter, Symbolic Diagrams-Translation to Mask Form.

(Text Book-1)

#### UNIT-III:

**Basic Circuit Concepts:** Sheet Resistance, Sheet Resistance concept applied to MOS transistors and Inverters, Area Capacitance of Layers, Standard unit of capacitance, Some area Capacitance Calculations, The Delay Unit, Inverter Delays, Driving large capacitive loads, Propagation Delays, Wiring Capacitances, Choice of layers.

**Scaling of MOS Circuits:** Scaling models and scaling factors, Scaling factors for device parameters, Limitations of scaling, Limits due to sub threshold currents, Limits on logic levels and supply voltage due to noise and current density. Switch logic, Gate logic.

(Text Book-1)



#### **UNIT-IV:**

**Chip Input and Output circuits:** ESD Protection, Input Circuits, Output Circuits and L(di/dt) Noise, On-Chip clock Generation and Distribution.

**Design for Testability:** Fault types and Models, Controllability and Observability, Ad Hoc Testable Design Techniques, Scan Based Techniques and Built-In Self Test techniques.

**(Text Book-2)**

#### **UNIT-V:**

**FPGA Design:** FPGA design flow, Basic FPGA architecture, FPGA Technologies, FPGA families- Altera Flex 8000FPGA, Altera Flex 10FPGA, Xilinx XC4000 series FPGA, Xilinx Spartan XL FPGA, Xilinx Spartan II FPGAs, Xilinx Vertex FPGA. Case studies: FPGA Implementation of Half adder and full adder.

**Introduction to synthesis:** Logic synthesis, RTL synthesis, High level Synthesis.

**(Reference Text Book-1)**

#### **UNIT-VI:**

**Introduction to Low Power VLSI Design:** Introduction to Deep submicron digital IC design, Low Power CMOS Logic Circuits: Over view of power consumption, Low –power design through voltage scaling, Estimation and optimisation of switching activity, Reduction of switching capacitance. Interconnect Design, Power Grid and Clock Design.

**(Text Book-2)**

#### **Text Books:**

1. Essentials of VLSI Circuits and Systems - Kamran Eshraghian, Douglas and A. Pucknell and Sholeh Eshraghian, Prentice-Hall of India Private Limited, 2005 Edition.
2. CMOS Digital Integrated Circuits Analysis and Design- Sung-Mo Kang, Yusuf Leblebici, Tata McGraw-Hill Education, 2003.

#### **References:**

1. Advanced Digital Design with the Verilog HDL, Michael D.Ciletti, Xilinx Design Series, Pearson Education
2. Analysis and Design of Digital Integrated Circuits in Deep submicron Technology, 3<sup>rd</sup> edition, David Hodges.

III Year - II Semester

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## DIGITAL SIGNAL PROCESSING

### OBJECTIVES

The student will be able to

- Analyze the Discrete Time Signals and Systems
- Know the importance of FFT algorithm for computation of Discrete Fourier Transform
- Understand the various implementations of digital filter structures
- Learn the FIR and IIR Filter design procedures
- Know the need of Multirate Processing
- Learn the concepts of DSP Processors

**UNIT I INTRODUCTION:** Introduction to Digital Signal Processing: Discrete time signals & sequences, Classification of Discrete time systems, stability of LTI systems, Invertability, Response of LTI systems to arbitrary inputs. Solution of Linear constant coefficient difference equations. Frequency domain representation of discrete time signals and systems. Review of Z-transforms, solution of difference equations using Z-transforms, System function.

**UNIT II DISCRETE FOURIER SERIES & FOURIER TRANSFORMS:** Properties of discrete Fourier series, DFS representation of periodic sequences, Discrete Fourier transforms: Properties of DFT, linear filtering methods based on DFT, Fast Fourier transforms (FFT) - Radix-2 decimation in time and decimation in frequency FFT Algorithms, Inverse FFT.

**UNIT III. DESIGN OF IIR DIGITAL FILTERS& REALIZATIONS:** Analog filter approximations – Butter worth and Chebyshev, Design of IIR Digital filters from analog filters, Design Examples, Analog and Digital frequency transformations. Basic structures of IIR systems, Transposed forms.

### UNIT IV DESIGN OF FIR DIGITAL FILTERS & REALIZATIONS:

Characteristics of FIR Digital Filters, frequency response. Design of FIR Digital Filters using Window Techniques and Frequency Sampling technique, Comparison of IIR & FIR filters.

Basic structures of FIR systems, *Lattice structures, Lattice-ladder structures*

**UNIT V MULTIRATE DIGITAL SIGNAL PROCESSING:** Introduction, Decimation, Interpolation Sampling rate conversion, Implementation of sampling rate converters, *Applications – Sub-band Coding of Speech Signals, Implementation of Digital Filter Banks, Trans-multiplexers.*

**UNIT VI INTRODUCTION TO DSP PROCESSORS:** Introduction to programmable DSPs: Multiplier and Multiplier Accumulator, Modified bus structures and memory access schemes in P-DSPs, Multiple Access Memory, Multiported memory, VLIW architecture, Pipelining, Special addressing modes, On-Chip Peripherals. Architecture of TMS320C5X: Introduction, Bus Structure, Central Arithmetic Logic Unit, Auxiliary Register ALU, Index Register, Block Move Address Register, Parallel Logic Unit, Memory mapped registers, program controller, some flags in the status registers, On-chip memory, On-chip peripherals.

**TEXT BOOKS:**

1. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G.Manolakis,Pearson Education / PHI, 2007.
2. Discrete Time Signal Processing – A.V.Oppenheim and R.W. Schaffer, PHI
3. Digital Signal Processors – Architecture, Programming and Applications,, B.Venkataramani, M.Bhaskar, TATA McGraw Hill, 2002
4. Digital Signal Processing – K Raja Rajeswari, I.K. International Publishing House

**Reference Books:**

1. Digital Signal Processing: Andreas Antoniou, TATA McGraw Hill , 2006
2. Digital Signal Processing: MH Hayes, Schaum's Outlines, TATA Mc-Graw Hill, 2007.
3. DSP Primer - C. Britton Rorabaugh, Tata McGraw Hill, 2005.
4. Fundamentals of Digital Signal Processing using Matlab – Robert J. Schilling, Sandra L. Harris,Thomson, 2007.
5. Digital Signal Processing – Alan V. Oppenheim, Ronald W. Schafer, PHI Ed., 2006
6. Digital Signal Processing – Ramesh babu, Sci Tech publications

**OUTCOMES**

After going through this course the student will be able to

- Apply the difference equations concept in the anayziation of Discrete time systems
- Use the FFT algorithm for solving the DFT of a given signal
- Design a Digital filter (FIR&IIR) from the given specifications
- Realize the FIR and IIR structures from the designed digital filter.
- Use the Multirate Processing concepts in various applications(eg: Design of phase shifters, Interfacing of digital systems...)
- Apply the signal processing concepts on DSP Processor.

III Year - II Semester

<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>4</b>	<b>0</b>	<b>0</b>	<b>3</b>

**OOPS THROUGH JAVA  
OPEN ELECTIVE**

**OBJECTIVES:**

- Understanding the OOP's concepts, classes and objects, threads, files, applets, swings and act.
- This course introduces computer programming using the JAVA programming language with object-oriented programming principles.
- Emphasis is placed on event-driven programming methods, including creating and manipulating objects, classes, and using Java for network level programming and middleware development

**UNIT-I:**

Introduction to OOP, procedural programming language and object oriented language, principles of OOP, applications of OOP, history of java, java features, JVM, program structure.

Variables, primitive data types, identifiers, literals, operators, expressions, precedence rules and associativity, primitive type conversion and casting, flow of control.

**UNIT-II:**

Classes and objects, class declaration, creating objects, methods, constructors and constructor overloading, garbage collector, importance of static keyword and examples, this keyword, arrays, command line arguments, nested classes.

**UNIT-III:**

Inheritance, types of inheritance, super keyword, final keyword, overriding and abstract class.

Interfaces, creating the packages, using packages, importance of CLASSPATH and java.lang package. Exception handling, importance of try, catch, throw, throws and finally block, user-defined exceptions, Assertions.

**UNIT-IV:**

Multithreading: introduction, thread life cycle, creation of threads, thread priorities, thread synchronization, communication between threads. Reading data from files and writing data to files, random access file,

**UNIT-V:**

Applet class, Applet structure, Applet life cycle, sample Applet programs. Event handling: event delegation model, sources of event, Event Listeners, adapter classes, inner classes.

**UNIT-VI:**

AWT: introduction, components and containers, Button, Label, Checkbox, Radio Buttons, List Boxes, Choice Boxes, Container class, Layouts, Menu and Scrollbar.

**OUTCOMES:**

- Understand Java programming concepts and utilize Java Graphical User Interface in Program writing.
- Write, compile, execute and troubleshoot Java programming for networking concepts.
- Build Java Application for distributed environment.
- Design and Develop multi-tier applications.

- Identify and Analyze Enterprise applications.

**TEXT BOOKS:**

1. The complete Reference Java, 8<sup>th</sup> edition, Herbert Schildt, TMH.
2. Programming in JAVA, Sachin Malhotra, Saurabh Choudary, Oxford.
3. Introduction to java programming, 7<sup>th</sup> edition by Y Daniel Liang, Pearson.

**REFERENCE BOOKS:**

1. Swing: Introduction, JFrame, JApplet, JPanel, Componets in Swings, Layout Managers in
2. Swings, JList and JScrollPane, Split Pane, JTabbedPane, JTree, JTable, Dialog Box.

## **DATA MINING OPEN ELECTIVE**

### **OBJECTIVES:**

- Students will be enabled to understand and implement classical models and algorithms in data warehousing and data mining.
- They will learn how to analyze the data, identify the problems, and choose the relevant models and algorithms to apply.
- They will further be able to assess the strengths and weaknesses of various methods and algorithms and to analyze their behavior.

### **UNIT –I**

**Introduction:** Why Data Mining? What Is Data Mining? 1.3 What Kinds of Data Can Be Mined? 1.4 What Kinds of Patterns Can Be Mined? Which Technologies Are Used? Which Kinds of Applications Are Targeted? Major Issues in Data Mining. Data Objects and Attribute Types, Basic Statistical Descriptions of Data, Data Visualization, Measuring Data Similarity and Dissimilarity

### **UNIT –II**

**Data Pre-processing:** Data Preprocessing: An Overview, Data Cleaning, Data Integration, Data Reduction, Data Transformation and Data Discretization

### **UNIT –III**

**Classification:** Basic Concepts, General Approach to solving a classification problem, Decision Tree Induction: Working of Decision Tree, building a decision tree, methods for expressing an attribute test conditions, measures for selecting the best split, Algorithm for decision tree induction.

### **UNIT –IV**

**Classification: Alternative Techniques,** Bayes' Theorem, Naïve Bayesian Classification, Bayesian Belief Networks

### **UNIT –V**

**Association Analysis: Basic Concepts and Algorithms:** Problem Defecation, Frequent Item Set generation, Rule generation, compact representation of frequent item sets, FP-Growth Algorithm. **(Tan & Vipin)**

### **UNIT –VI**

**Cluster Analysis: Basic Concepts and Algorithms: Overview:** What Is Cluster Analysis? Different Types of Clustering, Different Types of Clusters; K-means: The Basic K-means Algorithm, K-means Additional Issues, Bisecting K-means, Strengths and Weaknesses; Agglomerative Hierarchical Clustering: Basic Agglomerative Hierarchical Clustering Algorithm DBSCAN: Traditional Density Center-Based Approach, DBSCAN Algorithm, Strengths and Weaknesses. **(Tan & Vipin)**

### **OUTCOMES:**

- Understand stages in building a Data Warehouse
- Understand the need and importance of preprocessing techniques
- Understand the need and importance of Similarity and dissimilarity techniques
- Analyze and evaluate performance of algorithms for Association Rules.
- Analyze Classification and Clustering algorithms

**TEXT BOOKS:**

1. Introduction to Data Mining: Pang-Ning Tan & Michael Steinbach, Vipin Kumar, Pearson.
2. Data Mining concepts and Techniques, 3/e, Jiawei Han, Michel Kamber, Elsevier.

**REFERENCE BOOKS:**

1. Data Mining Techniques and Applications: An Introduction, Hongbo Du, Cengage Learning.
2. Data Mining : Vikram Pudi and P. Radha Krishna, Oxford.
3. Data Mining and Analysis - Fundamental Concepts and Algorithms; Mohammed J. Zaki, Wagner Meira, Jr, Oxford
4. Data Warehousing Data Mining & OLAP, Alex Berson, Stephen Smith, TMH.

## **INDUSTRIAL ROBOTICS**

### **OPEN ELECTIVE**

#### **Course Objectives:**

1. To give students practice in applying their knowledge of mathematics, science, and Engineering and to expand this knowledge into the vast area of robotics.
2. The students will be exposed to the concepts of robot kinematics, Dynamics, Trajectory planning.
3. Mathematical approach to explain how the robotic arm motion can be described.
4. The students will understand the functioning of sensors and actuators.

#### **UNIT-I**

**INTRODUCTION:** Automation and Robotics, CAD/CAM and Robotics – An over view of Robotics – present and future applications – classification by coordinate system and control system.

#### **UNIT – II**

**COMPONENTS OF THE INDUSTRIAL ROBOTICS:** Function line diagram representation of robot arms, common types of arms. Components, Architecture, number of degrees of freedom – Requirements and challenges of end effectors, determination of the end effectors, comparison of Electric, Hydraulic and Pneumatic types of locomotion devices.

#### **UNIT – III**

**MOTION ANALYSIS:** Homogeneous transformations as applicable to rotation and translation – problems.

**MANIPULATOR KINEMATICS:** Specifications of matrices, D-H notation joint coordinates and world coordinates Forward and inverse kinematics – problems.

#### **UNIT – IV**

Differential transformation and manipulators, Jacobians – problems

Dynamics: Lagrange – Euler and Newton – Euler formulations – Problems.

#### **UNIT V**

General considerations in path description and generation. Trajectory planning and avoidance of obstacles, path planning, Skew motion, joint integrated motion –straight line motion – Robot programming, languages and software packages-description of paths with a robot programming language..

#### **UNIT VI**

**ROBOT ACTUATORS AND FEED BACK COMPONENTS:**

Actuators: Pneumatic, Hydraulic actuators, electric & stepper motors.

Feedback components: position sensors – potentiometers, resolvers, encoders – Velocity sensors.

**ROBOT APPLICATIONS IN MANUFACTURING:** Material Transfer - Material handling, loading and unloading- Processing - spot and continuous arc welding & spray painting - Assembly and Inspection.



**TEXT BOOKS:**

1. Industrial Robotics / Groover M P / Pearson Edu.
2. Robotics and Control / Mittal R K & Nagrath I J / TMH.

**REFERENCES:**

1. Robotics / Fu K S/ McGraw Hill.
2. Robotic Engineering / Richard D. Klafter, Prentice Hall
3. Robot Analysis and Control / H. Asada and J.J.E. Slotine / BSP Books Pvt.Ltd.
4. Introduction to Robotics / John J Craig / Pearson Edu.

**Course outcomes:**

Upon successful completion of this course you should be able to:

1. Identify various robot configuration and components,
2. Select appropriate actuators and sensors for a robot based on specific application
3. Carry out kinematic and dynamic analysis for simple serial kinematic chains
4. Perform trajectory planning for a manipulator by avoiding obstacles.

# **POWER ELECTRONICS**

## **(Open Elective)**

### **Preamble:**

The usage of power electronics in day to day life has increased in recent years. It is important for student to understand the fundamental principles behind all these converters. This course covers characteristics of semiconductor devices, ac/dc, dc/dc, ac/ac and dc/ac converters. The importance of using pulse width modulated techniques to obtain high quality power supply (dc/ac converter) is also discussed in detail in this course.

### **Learning Objectives:**

- To study the characteristics of various power semiconductor devices and to design firing circuits for SCR.
- To understand the operation of single phase half wave and full-wave converters
- To understand the operation of different types of DC-DC converters.
- To understand the operation of inverters and application of PWM techniques for voltage control and harmonic mitigation.
- To understand the operation of AC-AC converters and switch mode power supplies operation.

### **UNIT-I**

#### **Power Semi-Conductor Devices**

Thyristors–Silicon controlled rectifiers (SCR's) – Characteristics of power MOSFET and power IGBT – Basic theory of operation of SCR–Static characteristics – Turn on and turn off methods–Dynamic characteristics of SCR – Snubber circuit design – Firing circuits for SCR

### **UNIT-II**

#### **AC-DC Single-Phase Converters**

Single phase half wave controlled rectifiers – R load and RL load with and without freewheeling diode – Single Phase full wave controlled rectifiers – center tapped configuration and bridge configuration – R load and RL load with and without freewheeling diode – Effect of source inductance in 1-phase fully controlled bridge rectifier.

### **UNIT-III**

#### **DC-DC Converters**

Buck Converter operation – Time ratio control and current limit control strategies–Voltage and current waveforms– Derivation of output voltage –Boost converter operation –Voltage and current waveforms–Derivation of output voltage – Buck-Boost converter operation –Voltage and current waveforms – Principle operation of forward and fly back converters

### **UNIT – IV**

#### **DC-AC Converters**

Single phase inverters–Unipolar and bipolar switching – Single phase half bridge and full bridge inverters with R and RL loads – PWM techniques– Sine triangular PWM technique– amplitude and frequency modulation Indices – Harmonic analysis.

### **UNIT – V**

#### **AC – AC Single-Phase Converters**

Static V-I characteristics of TRIAC and modes of operation – Single phase AC-AC regulator phase angle control and integrated cycle control with R and RL load – For continuous and discontinuous conduction – Principle of operation of Cyclo-Converters

### **UNIT – VI**

#### **Switch Mode Power Supplies**

Overview of Switching Power Supplies – Linear Power Supplies – DC to DC converters with electrical isolation – Control of Switch Mode DC Supplies – PWM duty ratio control – Current mode control – Power Supply Protection

**Learning Outcomes:**

Student should be able to

- Explain the characteristics of various power semiconductor devices and analyse the static and dynamic characteristics of SCR's.
- Design firing circuits for SCR.
- Able to explain the operation of single phase half wave and full-wave converters
- Analyse the operation of different types of DC-DC converters.
- Explain the operation of inverters and application of PWM techniques for voltage control and harmonic mitigation.
- Analyse the operation of AC-AC converters.
- Able to explain switch mode power supplies operation and control

**Text Books:**

1. Power Electronics: Circuits, Devices and Applications – by M. H. Rashid, Prentice Hall of India, 2nd edition, 1998
2. Power Electronics: Essentials & Applications by L.Umanand, Wiley, Pvt. Limited, India, 2009

**Reference Books:**

1. Power Electronics: converters, applications & design -by Nedmohan, Tore M. Undeland, Robbins by Wiley India Pvt. Ltd.
2. Elements of Power Electronics–Philip T.Krein.oxford.
3. Power Electronics – by P.S.Bhimbra, Khanna Publishers.
4. Power Electronics handbook by Muhammad H.Rashid, Elsevier.
5. Power Converter Circuits -by William Shepherd, Li zhang, CRC Taylor & Francis Group.

**BIO-MEDICAL ENGINEERING  
(OPEN ELECTIVE)**

**UNIT-I:**

**INTRODUCTION TO BIOMEDICAL INSTRUMENTATION:** Age of Biomedical Engineering, Development of Biomedical Instrumentation, Man Instrumentation System, Components of the Man-Instrument System, Physiological System of the Body, Problems Encountered in Measuring a Living System, Sources of Bioelectric Potentials, Muscle, Bioelectric Potentials, Sources of Bioelectric Potentials, Resting and Action Potentials, Propagation of Action Potential, Bioelectric Potentials-ECG, EEG and EMG, Evoked Responses.

**UNIT-II:**

**ELECTRODES AND TRANSDUCERS:** Introduction, Electrode Theory, Biopotential Electrodes, Examples of Electrodes, Basic Transducer Principles, Biochemical Transducers, The Transducer and Transduction Principles, Active Transducers, Passive Transducers, Transducers for Biomedical Applications, Pulse Sensors, Respiration Sensor, Transducers with Digital Output.

**UNIT-III:**

**CARDIOVASCULAR SYSTEM AND MEASUREMENTS:** The Heart and Cardiovascular System, Electro Cardiography, Blood Pressure Measurement, Measurement of Blood Flow and Cardiac Output, Measurement of Heart Sound, Plethysmography.

**MEASUREMENTS IN THE RESPIRATORY SYSTEM:** The Physiology of The Respiratory System, Tests and Instrumentation for The Mechanics of Breathing, Respiratory Therapy Equipment.

**UNIT-IV:**

**PATIENT CARE AND MONITORING:** Elements of Intensive-Care Monitoring, Patient Monitoring Displays, Diagnosis, Calibration and Repair ability of Patient-Monitoring Equipment, Other Instrumentation for Monitoring Patients, Organization of the Hospital for Patient-Care Monitoring, Pacemakers, Defibrillators, Radio Frequency Applications of Therapeutic use.

**THERAPEUTIC AND PROSTHETIC DEVICES:** Audiometers and Hearing Aids, Myoelectric Arm, Laparoscope, Ophthalmology Instruments, Anatomy of Vision, Electrophysiological Tests, Ophthalmoscope, Tonometer for Eye Pressure Measurement, Diathermy, Clinical Laboratory Instruments, Biomaterials, Stimulators.

**UNIT-V:**

**DIAGNOSTIC TECHNIQUES AND BIO-TELEMETRY:** Principles of Ultrasonic Measurement, Ultrasonic Imaging, Ultrasonic Applications of Therapeutic Uses, Ultrasonic Diagnosis, X-Ray and Radio-Isotope Instrumentations, CAT Scan, Emission Computerized Tomography, MRI, Introduction to Biotelemetry, Physiological Parameters Adaptable to Biotelemetry, The Components of Biotelemetry System, Implantable Units, Telemetry for ECG Measurements during Exercise, Telemetry for Emergency Patient Monitoring

**UNIT-VI:**

**MONITORS, RECORDERS AND SHOCK HAZARDS:** Biopotential Amplifiers, Monitors, Recorders, Shock Hazards and Prevention, Physiological Effects and Electrical Current, Shock Hazards from Electrical Equipment, Methods of Accident Prevention, Isolated Power Distribution System.

**Text Books:**

1. "Bio-Medical Electronics and Instrumentation", Onkar N. Pandey, Rakesh Kumar, Katson Books.
2. "Bio-Medical Instrumentation", Cromewell , Wiebell, Pfeiffer

**References:**

1. "Introduction to Bio-Medical Equipment Technology", 4<sup>th</sup> Edition, Joseph J. Carr, John M. Brown, Pearson Publications.
2. "Hand Book of Bio-Medical Instrumentation", Khandapur. McGrawHill

# ARTIFICIAL NEURAL NETWORKS

## OPEN ELECTIVE

### Course Objectives:

1. To Introduce the concept of Artificial Neural Networks , Characteristics, Models of Neuron, Learning Rules, Learning Methods, Stability and Convergence
2. To study the basics of Pattern Recognition and Feed forward Neural Networks
3. To study the basics of Feedback neural networks and Boltzmann machine
4. To introduce the Analysis of Feedback layer for different output functions, Pattern Clustering and Mapping networks
5. To study the Stability, Plasticity, Neocognitron and Different applications of Neural Networks

### UNIT-I : Basics of Artificial Neural Networks

Introduction: Biological Neural Networks, Characteristics of Neural Networks, Models of Neuron, Topology, Basic Learning Rules

Activation and Synaptic Dynamics: Activation Dynamic Models, Synaptic Dynamic Models, Learning Methods, Stability & Convergence, Recall in Neural Networks

**UNIT-II:** Functional Units of ANN for Pattern Recognition Tasks: Pattern Recognition problem Basic Fundamental Units, Pattern Recognition Tasks by the Functional Units

Feed forward Neural Networks: Analysis of Pattern Association Networks, Analysis of Pattern Classification Networks, Analysis of Pattern Mapping Networks

### UNIT-III:

Feedback Neural Networks: Analysis of linear auto adaptive feed forward networks, Analysis of pattern storage Networks, Stochastic Networks & Stimulated Annealing, Boltzmann machine

### UNIT-IV:

Competitive Learning Neural Networks: Components of a Competitive Learning Network, Analysis of Feedback layer for Different Output Functions, Analysis of Pattern Clustering Networks and Analysis of Feature Mapping Network

### UNIT-V:

Architectures for Complex Pattern Recognition Tasks: Associative memory, Pattern mapping Stability – Plasticity dilemma: ART, temporal patterns, Pattern visibility: Neocognitron

### UNIT-VI:

Applications of Neural Networks: Pattern classification, Associative memories, Optimization, Applications in Image Processing, Applications in decision making

**Text Book**

1. B.Yagnanarayana“Artificial Neural Networks”, PHI

**Reference Book**

1. Laurene Fausett ,“Fundamentals of Neural Networks”, Pearson Education
2. Simon Haykin , “Neural Networks”, Second Edition

**Course Outcomes**

1. This Course introduces Artificial Neural Networks and Learning Rules and Learning methods
2. Feed forward and Feedback Neural Networks are introduced
3. Applications of Neural Networks in different areas are introduced

III Year - II Semester

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## MICROPROCESSORS AND MICROCONTROLLERS LAB

### LIST OF EXPERIMENTS

#### **PART- A: (Minimum of 5 Experiments has to be performed)**

##### **8086 Assembly Language Programming using Assembler Directives**

15. Sorting.
16. Multibyte addition/subtraction
17. Sum of squares/cubes of a given n-numbers
18. Addition of n-BCD numbers
19. Factorial of given n-numbers
20. Multiplication and Division operations
21. Stack operations
22. BCD to Seven segment display codes

#### **PART- B: (Minimum of 3 Experiments has to be performed)**

##### **8086 Interfacing**

1. Hardware/Software Interrupt Application
2. A/D Interface through Intel 8255
3. D/A Interface through Intel 8255
4. Keyboard and Display Interface through Intel 8279
5. Generation of waveforms using Intel 8253/8254

#### **PART- C: (Minimum of 3 Experiments has to be performed)**

##### **8051 Assembly Language Programs**

1. Finding number of 1's and number of 0's in a given 8-bit number
2. Addition of even numbers from a given array
3. Ascending / Descending order
4. Average of n-numbers

#### **PART-D: (Minimum of 3 Experiments has to be performed)**

##### **8051 Interfacing**

1. Switches and LEDs
2. 7-Segment display (multiplexed)
3. Stepper Motor Interface
4. Traffic Light Controller



**Equipment Required:**

1. Regulated Power supplies
2. Analog/Digital Storage Oscilloscopes
3. 8086 Microprocessor kits
4. 8051 microcontroller kits
5. ADC module
6. DAC module
7. Stepper motor module
8. Keyboard module
9. LED, 7-Segment Units
10. Digital Multimeters
11. ROM/RAM Interface module
12. Bread Board etc.

**III Year - II Semester**

<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>0</b>	<b>0</b>	<b>3</b>	<b>2</b>

**VLSI LABORATORY**

**Note:** The students are required to design the schematic diagrams using CMOS logic and to draw the layout diagrams to perform the following experiments using 130nm technology with the Industry standard EDA Tools.

**List of Experiments:**

- i. Design and Implementation of an Universal Gates
- ii. Design and Implementation of an Inverter
- iii. Design and Implementation of Full Adder
- iv. Design and Implementation of Full Subtractor
- v. Design and Implementation of Decoder
- vi. Design and Implementation of RS-Latch
- vii. Design and Implementation of D-Latch
- viii. Design and Implementation asynchronous counter
- ix. Design and Implementation of static RAM cell
- x. Design and Implementation of 8 bit DAC using R-2R ladder network

**Software Required:**

- i. Mentor Graphics Software / Equivalent Industry Standard Software.
- ii. Personal computer system with necessary software to run the programs and to implement.

**III Year - II Semester**

<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>0</b>	<b>0</b>	<b>3</b>	<b>2</b>

**DIGITAL COMMUNICATIONS LAB**

1. Time division multiplexing.
2. Pulse code modulation.
3. Differential pulse code modulation.
4. Delta modulation.
5. Frequency shift keying.
6. Phase shift keying .
7. Differential phase shift keying.
8. Companding
9. Source Encoder and Decoder
10. Linear Block Code-Encoder and Decoder
11. Binary Cyclic Code – Encoder and Decoder
12. Convolution Code – Encoder and Decoder

**Equipment required for Laboratories:**

1. RPS – 0 – 30 V
2. CRO – 0 – 20 M Hz.
3. Function Generators – 0 – 1 M Hz
4. RF Generators – 0 – 1000 M Hz./0 – 100 M Hz.
5. Multimeters
6. Lab Experimental kits for Digital Communication
7. Components

III Year - II Semester

<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>0</b>	<b>2</b>	<b>0</b>	<b>0</b>

## INTELLECTUAL PROPERTY RIGHTS AND PATENTS

### UNIT I: Introduction to Intellectual Property Rights (IPR)

Concept of Property - Introduction to IPR – International Instruments and IPR - WIPO - TRIPS – WTO -Laws Relating to IPR - IPR Tool Kit - Protection and Regulation - Copyrights and Neighboring Rights – Industrial Property – Patents - Agencies for IPR Registration – Traditional Knowledge –Emerging Areas of IPR - Layout Designs and Integrated Circuits – Use and Misuse of Intellectual Property Rights.

### UNIT II: Copyrights and Neighboring Rights

Introduction to Copyrights – Principles of Copyright Protection – Law Relating to Copyrights - Subject Matters of Copyright – Copyright Ownership – Transfer and Duration – Right to Prepare Derivative Works –Rights of Distribution – Rights of Performers – Copyright Registration – Limitations – Infringement of Copyright – Relief and Remedy – Case Law - Semiconductor Chip Protection Act.

### UNIT III: Patents

Introduction to Patents - Laws Relating to Patents in India – Patent Requirements – Product Patent and Process Patent - Patent Search - Patent Registration and Granting of Patent - Exclusive Rights – Limitations - Ownership and Transfer — Revocation of Patent – Patent Appellate Board - Infringement of Patent – Double Patenting — Patent Cooperation Treaty – New developments in Patents – Software Protection and Computer related Innovations.

### UNIT IV: Trademarks

Introduction to Trademarks – Laws Relating to Trademarks – Functions of Trademark – Distinction between Trademark and Property Mark – Marks Covered under Trademark Law - Trade Mark Registration – Trade Mark Maintenance – Transfer of rights - Deceptive Similarities - Likelihood of Confusion - Dilution of Ownership – Trademarks Claims and Infringement – Remedies – Passing Off Action.

### UNIT V: Trade Secrets

Introduction to Trade Secrets – General Principles - Laws Relating to Trade Secrets - Maintaining Trade Secret – Physical Security – Employee Access Limitation – Employee Confidentiality Agreements – Breach of Contract – Law of Unfair Competition – Trade Secret Litigation – Applying State Law.

### UNIT VI: Cyber Law and Cyber Crime

Introduction to Cyber Law – Information Technology Act 2000 - Protection of Online and Computer Transactions - E-commerce - Data Security – Authentication and Confidentiality - Privacy - Digital Signatures – Certifying Authorities - Cyber Crimes - Prevention and Punishment – Liability of Network Providers.

- Relevant Cases Shall be dealt where ever necessary.

**References:**

1. Intellectual Property Rights (Patents & Cyber Law), Dr. A. Srinivas. Oxford University Press, New Delhi.
2. Deborah E.Bouchoux: Intellectual Property, Cengage Learning, New Delhi.
3. PrabhuddhaGanguli: Intellectual Property Rights, Tata Mc-Graw –Hill, New Delhi
4. Richard Stim: Intellectual Property, Cengage Learning, New Delhi.
5. Kompal Bansal &Parishit Bansal Fundamentals of IPR for Engineers, B. S. Publications (Press).
6. Cyber Law - Texts & Cases, South-Western's Special Topics Collections.
7. R.Radha Krishnan, S.Balasubramanian: Intellectual Property Rights, Excel Books. New Delhi.
8. M.Ashok Kumar and MohdIqbal Ali: Intellectual Property Rights, Serials Pub.

## **RADAR SYSTEMS**

### **OBJECTIVES**

#### **The student will be introduced to:**

1. The Basic Principle of radar and radar range equation.
2. Different types of radars; CW, FM-CW, MTI and pulse Doppler radars.
3. Understand the different tracking techniques for radar.
4. Understand the characteristics of a matched filter receiver and its performance.
5. Understand the different types of displays, duplexers and antennas used in radar systems.

#### **UNIT-I:**

**Basics of Radar** : Introduction, Maximum Unambiguous Range, simple Radar range Equation, Radar Block Diagram and Operation, Radar Frequencies and Applications. Prediction of Range Performance, Minimum Detectable Signal, Receiver Noise, Illustrative Problems.

**Radar Equation** : Modified Radar Range Equation, SNR, probability of detection, probability of False Alarm, Integration of Radar Pulses, Radar Cross Section of Targets (simple targets - sphere, cone-sphere), Creeping Wave, Transmitter Power, PRF and Range Ambiguities, System Losses (qualitative treatment), Illustrative Problems.

#### **UNIT-II:**

**CW and Frequency Modulated Radar** : Doppler Effect, CW Radar – Block Diagram, Isolation between Transmitter and Receiver, Non-zero IF Receiver, Receiver Bandwidth Requirements, Applications of CW radar. Illustrative Problems

**FM-CW Radar:** Range and Doppler Measurement, Block Diagram and Characteristics, FM-CW altimeter, Multiple Frequency CW Radar.

#### **UNIT-III:**

**MTI and Pulse Doppler Radar:** Introduction, Principle, MTI Radar with - Power Amplifier Transmitter and Power Oscillator Transmitter, Delay Line Cancellers – Filter Characteristics, Blind Speeds, Double Cancellation,  $N^{\text{th}}$  Cancellation Staggered PRFs. Range Gated Doppler Filters. MTI Radar Parameters, Limitations to MTI Performance, MTI versus Pulse Doppler Radar.

#### **UNIT -IV:**

**Tracking Radar:** Tracking with Radar, Sequential Lobing, Conical Scan, Mono pulse Tracking Radar – Amplitude Comparison Mono pulse (one- and two- coordinates), Phase Comparison Mono pulse, Tracking in Range, Acquisition and Scanning Patterns, Comparison of Trackers.

#### **UNIT -V:**

**Detection of Radar Signals in Noise** : Introduction, Matched Filter Receiver – Response Characteristics and Derivation, Correlation detection and Cross-correlation Receiver, Efficiency of Non-matched Filters, Matched Filter with Non-white Noise, Noise Figure and Noise Temperature.

#### **UNIT -VI:**

**Radar Receivers** –Displays – types. Duplexers – Branch type and Balanced type, Circulators as Duplexers. Introduction to Phased Array Antennas – Basic Concepts, Radiation Pattern, Beam Steering and Beam Width changes, Series versus parallel feeds, Applications, Advantages and Limitations. Radomes.

**TEXT BOOKS:**

1. Introduction to Radar Systems – Merrill I. Skolnik, TMH Special Indian Edition, 2<sup>nd</sup> Ed., 2007.

**REFERENCE BOOKS:**

1. Introduction to Radar Systems, 3<sup>rd</sup> edition – M.I. Skolnik, TMH Ed., 2005
2. Radar: Principles, Technology, Applications – Byron Edde, Pearson Education, 2004.
3. Radar Principles – Peebles, Jr., P.Z., Wiley, New York, 1998.
4. Principles of Modern Radar: Basic Principles – Mark A. Richards, James A. Scheer, William A. Holm, Yesdee,
5. Radar Engineering – GSN Raju, IK International.

**OUTCOMES****After going through this course the student will be able to:**

1. Derive the radar range equation and to solve some analytical problems.
2. Understand the different types of radars and its applications.
3. Understand the concept of tracking and different tracking techniques.
4. Understand the various components of radar receiver and its performanc.

IV Year - I Semester

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## DIGITAL IMAGE PROCESSING

### UNIT-1

**Introduction:** Introduction to Image Processing, Fundamental steps in digital image processing, components of an image processing system, image sensing and acquisition, image sampling and quantization, some basic relationships between pixels, an introduction to the mathematical tools used in digital image processing.

**Image Transforms:** Need for image transforms, Discrete Fourier transform (DFT) of one variable, Extension to functions of two variables, some properties of the 2-D Discrete Fourier transform, Importance of Phase, Walsh Transform. Hadamard transform, Haar Transform, Slant transform, Discrete Cosine transform, KL Transform, SVD and Radon Transform, Comparison of different image transforms

### UNIT-2

**Intensity Transformations and Spatial Filtering:** Background, Some basic intensity transformation functions, histogram processing, fundamentals of spatial filtering, smoothing spatial filters, sharpening spatial filters, Combining spatial enhancement methods

**Filtering in the Frequency Domain:** Preliminary concepts, The Basics of filtering in the frequency domain, image smoothing using frequency domain filters, Image Sharpening using frequency domain filters, Selective filtering.

### UNIT-3

**Image Restoration and Reconstruction:** A model of the image degradation / Restoration process, Noise models, restoration in the presence of noise only-Spatial Filtering, Periodic Noise Reduction by frequency domain filtering, Linear, Position –Invariant Degradations, Estimating the degradation function, Inverse filtering, Minimum mean square error (Wiener) filtering, constrained least squares filtering, geometric mean filter, image reconstruction from projections.

### UNIT-4

**Image compression:** Fundamentals, Basic compression methods: Huffman coding, Golomb coding, Arithmetic coding, LZW coding, Run-Length coding, Symbol-Based coding, Bit-Plane coding, Block Transform coding, Predictive coding

**Wavelets and Multiresolution Processing:** Image pyramids, subband coding, Multiresolution expansions, wavelet transforms in one dimensions & two dimensions, Wavelet coding.

### UNIT-5

**Image segmentation:** Fundamentals, point, line, edge detection, thresholding, region –based segmentation.

**Morphological Image Processing:** Preliminaries, Erosion and dilation, opening and closing, basic morphological algorithms for boundary extraction, thinning, gray-scale morphology, Segmentation using morphological watersheds.

### UNIT-6

**Color image processing:** color fundamentals, color models, pseudo color image processing, basics of full color image processing, color transformations, smoothing and sharpening. Image segmentation based on color, noise in color images, color image compression.



**Text Books**

1. R. C. Gonzalez and R. E. Woods, Digital Image Processing, 3<sup>rd</sup> edition, Prentice Hall, 2008.
2. Jayaraman, S. Esakkirajan, and T. Veerakumar, "Digital Image Processing", Tata McGraw-Hill Education, 2011.

**Reference Books**

1. Anil K.Jain, "Fundamentals of Digital Image Processing", Prentice Hall of India, 9<sup>th</sup> Edition, Indian Reprint, 2002.
2. B.Chanda, D.Dutta Majumder, "Digital Image Processing and Analysis", PHI, 2009.

**Course Objectives:**

Students undergoing this course are expected to:

1. Familiarize with basic concepts of digital image processing and different image transforms
2. Learn various image processing techniques like image enhancement, restoration, segmentation and compression
3. Understand color fundamentals and different color models
4. Understand wavelets and morphological image processing

**Course Outcomes:**

After undergoing the course students will be able to

1. Perform image manipulations and different digital image processing techniques
2. Perform basic operations like – Enhancement, segmentation, compression, Image transforms and restoration techniques on image.
3. Analyze pseudo and fullcolor image processing techniques.
4. Apply various morphological operators on images

**IV Year - I Semester**

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## **COMPUTER NETWORKS**

### **OBJECTIVES:**

- Understand state-of-the-art in network protocols, architectures, and applications.
- Process of networking research
- Constraints and thought processes for networking research
- Problem Formulation—Approach—Analysis—

### **UNIT – I**

Introduction: Network Topologies WAN, LAN, MAN. Reference models- The OSI Reference Model- the TCP/IP Reference Model - A Comparison of the OSI and TCP/IP Reference Models

### **UNIT – II**

Physical Layer – Fourier Analysis – Bandwidth Limited Signals – The Maximum Data Rate of a Channel - Guided Transmission Media, Digital Modulation and Multiplexing: Frequency Division Multiplexing, Time Division Multiplexing, Code Division Multiplexing

Data Link Layer Design Issues, Error Detection and Correction, Elementary Data Link Protocols, Sliding Window Protocols

### **UNIT – III**

The Data Link Layer - Services Provided to the Network Layer – Framing – Error Control – Flow Control, Error Detection and Correction – Error-Correcting Codes – Error Detecting Codes, Elementary Data Link Protocols- A Utopian Simplex Protocol-A Simplex Stop and Wait Protocol for an Error free channel-A Simplex Stop and Wait Protocol for a Noisy Channel, Sliding Window Protocols-A One Bit Sliding Window Protocol-A Protocol Using Go-Back-N- A Protocol Using Selective Repeat

### **UNIT – IV**

The Medium Access Control Sublayer-The Channel Allocation Problem-Static Channel Allocation-Assumptions for Dynamic Channel Allocation, Multiple Access Protocols-Aloha-Carrier Sense Multiple Multiple Access Protocols-Collision-Free Protocols-Limited Contention Protocols-Wireless LAN Protocols, Ethernet-Classic Ethernet Physical Layer-Classic Ethernet MAC Sublayer Protocol-Ethernet Performance-Fast Ethernet Gigabit Ethernet-10-Gigabit Ethernet-Retrospective on Ethernet, Wireless Lans-The 802.11 Architecture and Protocol Stack-The 802.11 Physical Layer-The802.11 MAC Sublayer Protocol-The 805.11 Frame Structure-Services

### **UNIT – V**

Design Issues-The Network Layer Design Issues – Store and Forward Packet Switching-Services Provided to the Transport layer- Implementation of Connectionless Service-Implementation of Connection Oriented Service-Comparison of Virtual Circuit and Datagram Networks, Routing Algorithms-The Optimality principle-Shortest path Algorithm, Congestion Control Algorithms-Approaches to Congestion Control-Traffic Aware Routing-Admission Control-Traffic Throttling-Load Shedding.

### **UNIT – VI**

Transport Layer – The Internet Transport Protocols: Udp, the Internet Transport Protocols: Tcp  
Application Layer –The Domain Name System: The DNS Name Space, Resource Records, Name Servers, Electronic Mail: Architecture and Services, The User Agent, Message Formats, Message Transfer, Final Delivery

**OUTCOMES:**

- Understand OSI and TCP/IP models
- Analyze MAC layer protocols and LAN technologies
- 3 .Design applications using internet protocols
- 4 .Understand routing and congestion control algorithms
- 5 .Understand how internet works

**TEXT BOOKS:**

1. Computer Networks, Tanenbaum and David J Wetherall, 5th Edition, Pearson Edu, 2010
2. Computer Networks: A Top Down Approach, Behrouz A. Forouzan, Firouz Mosharraf, McGraw Hill Education

**REFERENCE BOOKS:**

1. Larry L. Peterson and Bruce S. Davie, “Computer Networks - A Systems Approach” (5th ed), Morgan Kaufmann/ Elsevier, 2011

**IV Year - I Semester**

<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>4</b>	<b>0</b>	<b>0</b>	<b>3</b>

## **OPTICAL COMMUNICATIONS**

### **OBJECTIVES**

The student will be introduced to the functionality of each of the components that comprise a fiber-optic communication system

- the properties of optical fiber that affect the performance of a communication link and types of fiber materials with their properties and the losses occur in fibers.
- the principles of single and multi-mode optical fibers and their characteristics
- working of semiconductor lasers, and differentiate between direct modulation and external electro-optic modulation.
- Analyze the operation of LEDs, laser diodes, and PIN photo detectors (spectral properties, bandwidth, and circuits) and apply in optical systems.
- Analyze and design optical communication and fiber optic sensor systems.
- the models of analog and digital receivers.

### **UNIT I**

Overview of optical fiber communication - Historical development, The general system, advantages of optical fiber communications. Optical fiber wave guides- Introduction, Ray theory transmission, Total Internal Reflection, Acceptance angle, Numerical Aperture, Skew rays, Cylindrical fibers- Modes, V-number, Mode coupling, Step Index fibers, Graded Index fibers, Single mode fibers- Cut off wavelength, Mode Field Diameter, Effective Refractive Index, Related problems.

### **UNIT II**

Fiber materials:- Glass, Halide, Active glass, Chalcogenide glass, Plastic optical fibers. Signal distortion in optical fibers-Attenuation, Absorption, Scattering and Bending losses, Core and Cladding losses, Information capacity determination, Group delay, Types of Dispersion:- Material dispersion, Wave-guide dispersion, Polarization-Mode dispersion, Intermodal dispersion, Pulse broadening in Graded index fiber, Related problems.

### **UNIT III**

. Optical fiber Connectors-Connector types, Single mode fiber connectors, Connector return loss, Fiber Splicing- Splicing techniques, Splicing single mode fibers, Fiber alignment and joint loss- Multimode fiber joints, single mode fiber joints.

### **UNIT IV**

Optical sources- LEDs, Structures, Materials, Quantum efficiency, Power, Modulation, Power bandwidth product. Injection Laser Diodes- Modes, Threshold conditions, External quantum efficiency, Laser diode rate equations, Resonant frequencies, Reliability of LED&ILD, Optical detectors- Physical principles of PIN and APD, Detector response time, Temperature effect on Avalanche gain, Comparison of Photo detectors, Related problems.

### **UNIT V**

Source to fiber power launching - Output patterns, Power coupling, Power launching, Equilibrium Numerical Aperture, Laser diode to fiber coupling, Optical receiver operation- Fundamental receiver operation, Digital signal transmission, error sources, Receiver configuration, Digital receiver performance, Probability of Error, Quantum limit, Analog receivers.

## **UNIT VI**

Optical system design - Point-to- point links- Component choice and considerations, Link power budget, Rise time budget with examples, Line coding in Optical links, WDM, Necessity, Principles, Measurement of Attenuation and Dispersion, Eye pattern.

### **TEXT BOOKS :**

1. Optical Fiber Communications – Gerd Keiser, Mc Graw-Hill International edition, 3rd Edition, 2000.
2. Optical Fiber Communications – John M. Senior, PHI, 2nd Edition, 2002.

### **REFERENCES :**

1. Fiber Optic Communications – D.K. Mynbaev , S.C. Gupta and Lowell L. Scheiner, Pearson Education,2005.
2. Text Book on Optical Fiber Communication and its Applications – S.C.Gupta, PHI, 2005.
3. Fiber Optic Communication Systems – Govind P. Agarwal , John Wiley, 3rd Edition, 2004.
4. Fiber Optic Communications – Joseph C. Palais, 4th Edition, Pearson Education, 2004.

### **OUTCOMES**

After going through this course the student will be able to

- Choose necessary components required in modern optical communications systems .
- Design and build optical fiber experiments in the laboratory, and learn how to calculate electromagnetic modes in waveguides, the amount of light lost going through an optical system, dispersion of optical fibers.
- Use different types of photo detectors and optical test equipment to analyze optical fiber and light wave systems.
- Choose the optical cables for better communication with minimum losses  
Design, build, and demonstrate optical fiber experiments in the laboratory.

IV Year - I Semester

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**TELEVISION ENGINEERING**  
**(Elective- I)**

**UNIT I**

**INTRODUCTION:** TV transmitter and receivers, synchronization. Television Pictures: Geometric form and aspect ratio, image continuity, interlaced scanning, picture resolution, Composite video signal: Horizontal and vertical sync, scanning sequence, Colour signal generation and Encoding: Perception of brightness and colours, additive colour mixing, video signals for colours, luminance signal, colour difference signals, encoding of colour difference signals, formation of chrominance signals, PAL encoder.

**UNIT II**

**TV SIGNAL TRANSMISSION AND PROPAGATION:** Picture signal transmission, positive and negative modulation, VSB transmission, sound signal transmission, standard channel BW, TV transmitter, TV signal propagation, interference, TV broadcast channels.

**MONOCHROME TV RECEIVER:** RF tuner, IF subsystem, video amplifier, sound section, sync separation and processing, deflection circuits, scanning circuits.

**PAL-D colour receiver:** Electron tuners, IF subsystem, Y-signal channel, chroma decoder, separation of U & V Colour phasors, synchronous demodulators, subcarrier generation, raster circuits.

**UNIT III**

**VISION IF SUBSYSTEM:** AGC, noise cancellation, video and intercarrier sound signal detection, Colour receiver IF subsystem, Receiver sound system: FM detection, FM Sound detectors, typical applications. TV Receiver Tuners: Tuner operation, VHF and UHF tuners.

**COLOUR SIGNAL DECODING:** PAL-D decoder, chroma signal amplifiers, separation of U and V signals, Color burst separation, Burst phase discriminator, Reference oscillator, Indent and color killer circuits, RO phase shift and 180 degrees PAL-SWITCH circuitry, U & V demodulators, Colour signal mixing.

**UNIT-IV**

**HISTORY OF HDTV:** Analog and Digital TV Compared, Going HD, Broadcast Engineering and Information Technology, The Road to HDTV, The Grand Alliance, A DTV Standard at Last, Producing HDTV, HD Goes Coast-to-Coast, DTV Conversion.

**COMPRESSION TECHNIQUES:** Compression, MPEG-2 Video Compression, MPEG-4, H.264, Motion – JPEG (M-JPEG) compression, Audio Compression, Compressed Data Streams, Packetized Transport.

**UNIT V**

**DTV TRANSMITTER AND RECIEVER:** Engineering Basics, Presentation, Transmission, Reception and Demodulation, Transport Stream Demultiplexing, Decoding and Decompression, Program Assembly and Presentation, Receiver Issues, Presentation Concerns.

**HDTV AND DTV STANDARDS:** Standards Bodies, The ATSC Standards, SMPTE Standards, The Audio Engineering Society, Cable DTV Standards, Institute of Electronic and Electrical Engineers, The Consumer Electronics Association, Other Societies and Organizations.

**UNIT VI**

**EMERGING TECHNOLOGIES AND STANDARDS:** Technology and Standards Development, Presentation, Delivery and Distribution, MPEG and Metadata, Enhanced, Interactive and Personalized, Virtual Product Placement, Multiplatform Emergency Alert System.

## **TEXT BOOKS**

1. Modern Television Practice – Principles, Technology and Service – R.R.Gulati, New Age International Publication, 2002
2. Television and Video Engineering – A.M.Dhake, 2<sup>nd</sup> Edition,
3. “HDTV and the Transition to Digital Broadcasting: Understanding New Television Technologies” by Philip J. Cianci, Focal Press, 2007.
4. “Digital Video and HDTV Algorithms and Interfaces” by Charles Poynton, Morgan Kaufman publishers, 2007.

## **REFERENCES**

1. Basic Television and Video Systems – B.Grob and C.E.Herndon, McGrawHill,1999
2. “Newnes Guide to Television and Video Technology” by Ibrahim.K.F, Newnes Publishers, 4<sup>th</sup> edition, 2007.
3. “H.264 and MPEG-4 and Video compression video coding for Next-generation Multimedia” by Iain E. G. Richardson,John Wiley & Sons Ltd., 2003.

“.

## **ELECTRONIC SWITCHING SYSTEMS**

### **(Elective- I)**

#### **OBJECTIVES :**

The student will

- Understand the means of measuring traffic.
- Understand the implication of the traffic level on system design.

#### **UNIT -I:**

**Introduction:** Evolution of Telecommunications, Simple Telephone Communication, Basics of Switching System, Manual Switching System, Major Telecommunication Networks.

**Crossbar Switching:** Principles of Common Control, Touch Tone Dial Telephone, Principles of Crossbar Switching, Crossbar Switch Configurations, Cross point Technology, Crossbar Exchange Organization.

#### **UNIT -II:**

**Electronic Space Division Switching:** Stored Program Control, Centralized SPC: Stand by mode, Synchronous duplex mode, Distributed SPC, Software Architecture, Application Software, Enhanced Services, Two-Stage Networks, Three-Stage Networks, n- Stage Networks.

#### **UNIT -III**

**Time Division Switching:** Basic Time Division Space Switching, Basic Time Division Time Switching, Generalised time division Space switch, Basic Time division time switching: modes of operation, simple problems, Time Multiplexed Space Switching, Time Multiplexed Time division space Switch, Time Multiplexed Time Switching, Combination Switching: Time Space (TS) Switching, Space-time (ST) Switching, Three-Stage Combination Switching, n- Stage Combination Switching.

#### **UNIT IV**

**Telephone Networks:** Subscriber Loop System, Switching Hierarchy and Routing, Transmission Plan, Transmission Systems, Numbering Plan, Charging Plan, Signaling Techniques, In-channel Signaling, Common Channel Signaling, CCITT Signaling System no.6, CCITT Signaling System no.7, **Packet Switching:** Statistical Multiplexing, Local- Area and Wide- Area Networks, Large-scale Networks, Broadband Networks.

#### **UNIT -V:**

**Switching Networks:** Single- Stage Networks, Grading, Link Systems, Grades of service of link systems, Application of Graph Theory to link Systems, Use of Expansion, Call Packing, Rearrange-able Networks, Strict- Sense non-blocking Networks, Sectionalized Switching Networks

**Telecommunications Traffic:** The Unit of Traffic, Congestion, Traffic Measurement, A Mathematical Model, Lost-call Systems, Queuing Systems. Problems

#### **UNIT -VI:**

**Integrated Services Digital Network:** Motivation for ISDN, New Services, Network and Protocol Architecture, Transmission Channels, User- Network Interfaces, Signaling, Numbering and Addressing, Service Characterization, Interworking, ISDN Standards, Expert Systems in ISDN, Broadband ISDN, Voice Data Integration.

#### **TEXT BOOKS:**

1. Telecommunication Switching Systems and Networks- Thiagarajan Viswanathan, 2000, PHI.
2. Telecommunications Switching, Traffic and Networks- J. E. Flood, 2006, Pearson Education.



**REFERENCES:**

1. Digital Telephony- J. Bellamy, 2nd Edition, 2001, John Wiley.
2. Data Communications and Networks- Achyut S. Godbole, 2004, TMH.
3. Principles of Communication Systems- H. Taub & D. Schilling, 2nd Edition, 2003, TMH.
4. Data Communication & Networking- B. A. Forouzan, 3rd Edition, 2004, TMH.
5. Telecommunication System Engineering – Roger L. Freeman, 4th Ed., Wiley-Inter Science, John Wiley & Sons, 2004.

**Outcomes**

The student will be able to

- Evaluate the time and space parameters of a switched signal
- Establish the digital signal path in time and space, between two terminals
- Evaluate the inherent facilities within the system to test some of the SLIC, CODEC and digital switch functions.
- Investigate the traffic capacity of the system.
- Evaluate methods of collecting traffic data.
- Evaluate the method of interconnecting two separate digital switches.

## **SYSTEM DESIGN THROUGH VERILOG** (Elective- I)

### **UNIT-I**

#### **INTRODUCTION TO VERILOG:**

Verilog as HDL, Levels of design description, concurrency, simulation and synthesis, functional verification, system tasks, programming language interface(PLI), module, simulation and synthesis tools, test benches.

#### **LANGUAGE CONSTRUCTS AND CONVENTIONS:**

Introduction, keywords, identifiers, whitespace characters, comments, numbers, strings, logic values, data types, scalars and vectors, parameters, memory, operators, system tasks.

### **UNIT-II**

#### **GATE LEVEL MODELLING:**

Introduction, AND gate primitive, module structure, other gate primitives, illustrative examples, tristate gates, array of instances of primitives, design of Flip flops with gate primitives, delays, strengths and contention resolution, net types, design of basic circuits.

### **UNIT-III**

#### **BEHAVIORAL MODELLING:**

Introduction, operations and assignments, functional Bifurcation, initial construct, always construct, examples, assignments with delays, wait construct, multiple always blocks, designs at behavioral level, blocking and non-blocking assignments, the case statement, simulation flow, if and if else constructs, assign-De assign construct, repeat construct, FOR loop, the disable construct, While loop, Forever loop, parallel blocks, force-release construct, event.

### **UNIT-IV**

#### **DATAFLOW LEVEL AND SWITCH LEVEL MODELLING:**

Introduction, continuous assignment structures, delays and continuous assignments, assignment to vectors, basic transistor switches, CMOS switch, Bidirectional gates and time delays with switch primitives, instantiations with strengths and delays, strength contention with trireg nets.

### **UNIT-V**

**SYNTHESIS OF COMBINATIONAL AND SEQUENTIAL LOGIC USING VERILOG:** Synthesis of combinational logic: Net list of structured primitives, a set of continuous assignment statements and level sensitive cyclic behavior with examples, Synthesis of priority structures, Exploiting logic don't care conditions. Synthesis of sequential logic with latches: Accidental synthesis of latches and Intentional synthesis of latches, Synthesis of sequential logic with flip-flops, Synthesis of explicit state machines.

### **UNIT-VI**

#### **VERILOG MODELS:**

Static RAM Memory, A simplified 486 Bus Model, Interfacing Memory to a Microprocessor Bus, UART Design and Design of Microcontroller CPU.

#### **TEXT BOOKS:**

1. Design through Verilog HDL – T.R. Padmanabhan and B. Bala Tripura Sundari, WSE, IEEE Press, 2004.
2. Advanced Digital Design with Verilog HDL – Michael D. Ciletti, PHI, 2005.

#### **REFERENCES:**

1. Fundamentals of Logic Design with Verilog – Stephen. Brown and Zvonko Vranesic, TMH, 2005.
2. A Verilog Primer – J. Bhasker, BSP, 2003.

IV Year - I Semester

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**EMBEDDED SYSTEMS  
ELECTIVE - II**

**OBJECTIVES:**

**The main objectives of this course are given below:**

- The basic concepts of an embedded system are introduced.
- The various elements of embedded hardware and their design principles are explained.
- Different steps involved in the design and development of firmware for embedded systems is elaborated.
- Internals of Real-Time operating system and the fundamentals of RTOS based embedded firmware design is discussed.
- Fundamental issues in hardware software co-design were presented and explained.
- Familiarise with the different IDEs for firmware development for different family of processors/controllers and embedded operating systems.
- Embedded system implementation and testing tools are introduced and discussed.

**Outcomes:**

**At the end of this course the student can able to:**

- Understand the basic concepts of an embedded system and able to know an embedded system design approach to perform a specific function.
- The hardware components required for an embedded system and the design approach of an embedded hardware.
- The various embedded firmware design approaches on embedded environment.
- Understand how to integrate hardware and firmware of an embedded system using real time operating system.

**Syllabus**

**UNIT-I**

**INTRODUCTION:** Embedded system-Definition, history of embedded systems, classification of embedded systems, major application areas of embedded systems, purpose of embedded systems, the typical embedded system-core of the embedded system, Memory, Sensors and Actuators, Communication Interface, Embedded firmware, Characteristics of an embedded system, Quality attributes of embedded systems, Application-specific and Domain-Specific examples of an embedded system.

**UNIT-II**

**EMBEDDED HARDWARE DESIGN:** Analog and digital electronic components, I/O types and examples, Serial communication devices, Parallel device ports, Wireless devices, Timer and counting devices, Watchdog timer, Real time clock.

### **UNIT-III**

**EMBEDDED FIRMWARE DESIGN:** Embedded Firmware design approaches, Embedded Firmware development languages, ISR concept, Interrupt sources, Interrupt servicing mechanism, Multiple interrupts, DMA, Device driver programming, Concepts of C versus Embedded C and Compiler versus Cross-compiler.

### **UNIT-IV**

**REAL TIME OPERATING SYSTEM:** Operating system basics, Types of operating systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling, Threads, Processes and Scheduling, Task communication, Task synchronisation, Device Drivers.

**HARDWARE SOFTWARE CO-DESIGN:** Fundamental Issues in Hardware Software Co-Design, Computational models in embedded design, Hardware software Trade-offs, Integration of Hardware and Firmware, ICE.

### **UNIT-V**

**EMBEDDED SYSTEM DEVELOPMENT:** The integrated development environment, Types of files generated on cross-compilation, Deassembler/Decompiler, Simulators, Emulators and Debugging, Target hardware debugging, Boundary Scan, Embedded Software development process and tools.

### **UNIT-VI**

**EMBEDDED SYSTEM IMPLEMENTATION AND TESTING:** The main software utility tool, CAD and the hardware, Translation tools-Pre-processors, Interpreters, Compilers and Linkers, Debugging tools, Quality assurance and testing of the design, Testing on host machine, Simulators, Laboratory Tools.

### **Text Books:**

1. Embedded Systems Architecture- By Tammy Noergaard, Elsevier Publications, 2013.
2. Embedded Systems-By Shibu.K.V-Tata McGraw Hill Education Private Limited, 2013.

### **References:**

1. Embedded System Design, Frank Vahid, Tony Givargis, John Wiley Publications, 2013.
2. Embedded Systems-Lyla B.Das-Pearson Publications, 2013.

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## ANALOG IC DESIGN

### ELECTIVE - II

#### OBJECTIVES

The student will be introduced to

- The student will be able to understand the behavior of MOS Devices and Small-Signal & Large-Signal Modeling of MOS Transistor and Analog Sub-Circuits.
- In this course, students can study CMOS Amplifiers like Differential Amplifiers, Cascode Amplifiers, Output Amplifiers, and Operational Amplifiers.
- Another main object of this course is to motivate the graduate students to design and to develop the Analog CMOS Circuits for different Analog operations.
- The concepts of Open-Loop Comparators and Different Types of Oscillators like Ring Oscillator, LC Oscillator etc.

#### UNIT -I:

**MOS Devices and Modeling:** The MOS Transistor, Passive Components- Capacitor & Resistor, Integrated circuit Layout, CMOS Device Modeling - Simple MOS Large-Signal Model, Other Model Parameters, Small-Signal Model for the MOS Transistor, Computer Simulation Models, Sub-threshold MOS Model.

#### UNIT -II:

**Analog CMOS Sub-Circuits:** MOS Switch, MOS Diode, MOS Active Resistor, Current Sinks and Sources, Current Mirrors-Current mirror with Beta Helper, Degeneration, Cascode current Mirror and Wilson Current Mirror, Current and Voltage References, Band gap Reference.

#### UNIT -III:

**CMOS Amplifiers:** Inverters, Differential Amplifiers, Cascode Amplifiers, Current Amplifiers, Output Amplifiers, High Gain Amplifiers Architectures.

#### UNIT -IV:

**CMOS Operational Amplifiers:** Design of CMOS Op Amps, Compensation of Op Amps, Design of Two-Stage Op Amps, Power- Supply Rejection Ratio of Two-Stage Op Amps, Cascode Op Amps, Measurement Techniques of OP Amp.

#### UNIT -V:

**Comparators:** Characterization of Comparator, Two-Stage, Open-Loop Comparators, Other Open-Loop Comparators, Improving the Performance of Open-Loop Comparators, Discrete-Time Comparators.

## **UNIT -VI:**

**Oscillators & Phase-Locked Loops:** General Considerations, Ring Oscillators, LC Oscillators, Voltage Controlled Oscillators.

Simple PLL, Charge Pump PLLs, Non-Ideal Effects in PLLs, Delay Locked Loops, Applications.

### **Text Books:**

1. Design of Analog CMOS Integrated Circuits- Behzad Razavi, TMH Edition.
2. CMOS Analog Circuit Design - Philip E. Allen and Douglas R. Holberg, Oxford University Press, International Second Edition/Indian Edition, 2010.

### **References:**

1. Analysis and Design of Analog Integrated Circuits- Paul R. Gray, Paul J. Hurst, S. Lewis and R. G. Meyer, Wiley India, Fifth Edition, 2010.
2. Analog Integrated Circuit Design- David A. Johns, Ken Martin, Wiley Student Edn, 2013.

## **OUTCOMES**

After going through this course the student will be able to

- Understand the concepts of MOS Devices and Modeling.
- Design and analyze any Analog Circuits in real time applications.
- Extend the Analog Circuit Design to Different Applications in Real Time.
- Understand of Open-Loop Comparators and Different Types of Oscillators.

## **NETWORK SECURITY AND CRYPTOGRAPHY ELECTIVE - II**

### **OBJECTIVES:**

- In this course the following principles and practice of cryptography and network security are covered:
- Classical systems, symmetric block ciphers (DES, AES, other contemporary symmetric ciphers)
- Public-key cryptography (RSA, discrete logarithms),
- Algorithms for factoring and discrete logarithms, cryptographic protocols, hash functions, authentication, key management, key exchange, signature schemes,
- Email and web security, viruses, firewalls, digital right management, and other topics.

### **UNIT- I:**

#### **Basic Principles**

Security Goals, Cryptographic Attacks, Services and Mechanisms, Mathematics of Cryptography.

### **UNIT- II:**

#### **Symmetric Encryption**

Mathematics of Symmetric Key Cryptography, Introduction to Modern Symmetric Key Ciphers, Data Encryption Standard, Advanced Encryption Standard.

### **UNIT- III:**

#### **Asymmetric Encryption**

Mathematics of Asymmetric Key Cryptography, Asymmetric Key Cryptography

### **UNIT- IV:**

#### **Data Integrity, Digital Signature Schemes & Key Management**

Message Integrity and Message Authentication, Cryptographic Hash Functions, Digital Signature, Key Management.

### **UNIT -V:**

#### **Network Security-I**

Security at application layer: PGP and S/MIME, Security at the Transport Layer: SSL and TLS

### **UNIT -VI:**

#### **Network Security-II**

Security at the Network Layer: IPSec, System Security

### **OUTCOMES:**

- To be familiarity with information security awareness and a clear understanding of its importance.
- To master fundamentals of secret and public cryptography
- To master protocols for security services
- To be familiar with network security threats and countermeasures
- To be familiar with network security designs using available secure solutions (such as PGP, SSL, IPSec, etc)

### **TEXT BOOKS:**

1. Cryptography and Network Security, Behrouz A Forouzan, Debdeep Mukhopadhyay, (3e) Mc Graw Hill.
2. Cryptography and Network Security, William Stallings, (6e) Pearson.
3. Everyday Cryptography, Keith M. Martin, Oxford.

### **REFERENCE BOOKS:**

1. Network Security and Cryptography, Bernard Meneges, Cengage Learning.

IV Year - I Semester

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**MICROWAVE ENGINEERING & OPTICAL LAB**

**Minimum Twelve Experiments to be conducted:**

**Part – A (Any 7 Experiments ( 8 & 9 compulsory)) :**

1. Reflex Klystron Characteristics.
2. Gunn Diode Characteristics.
3. Attenuation Measurement.
4. Directional Coupler Characteristics.
5. Impedance and Frequency Measurement.
6. Scattering parameters of Circulator.
7. Scattering parameters of Magic Tee.
8. Radiation Pattern of Horn and Parabolic Antennas.
9. Synthesis of Microstrip antennas (Rectangular Structure) Using HFSS.

**Part – B (Any 5 Experiments) :**

10. Characterization of LED.
11. Characterization of Laser Diode.
12. Intensity modulation of Laser output through an optical fiber.
13. Measurement of Data rate for Digital Optical link.
14. Measurement of NA.
15. Measurement of losses for Analog Optical link.

**Equipment required for Laboratories:**

1. Regulated Klystron Power Supply, Klystron mount
2. VSWR Meter
3. Micro Ammeter
4. Multi meter
5. CRO
6. GUNN Power Supply, Pin Modulator
7. Crystal Diode detector
8. Micro wave components (Attenuation)
9. Frequency Meter
10. Slotted line carriage
11. Probe detector
12. Wave guide shorts
13. SS Tuner
14. Directional Coupler
15. E, H, Magic Tees
16. Circulators, Isolator
17. Matched Loads
18. Pyramidal Horn and Parabolic Antennas
19. Turntable for Antenna Measurements
20. HFSS Software
21. Fiber Optic Analog Trainer based LED
22. Fiber Optic Analog Trainer based laser
23. Fiber Optic Digital Trainer
24. Fiber cables - (Plastic, Glass)



IV Year - I Semester

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<b>0</b>	<b>0</b>	<b>3</b>	<b>2</b>

**DIGITAL SIGNAL PROCESSING LABORATORY**

**List of the Experiments / programs**

To Student has to perform at least FOUR Experiments in each part

**PART-1( SIGNALS )**

- 1) Generation of discrete time signals for discrete signals
- 2) To verify the Linear Convolution
  - a) Using MATLAB
  - b) Using Code Composer Studio(CCS)
- 3) To verify the Circular Convolution for discrete signals
  - a) Using MATLAB
  - b) Using Code Composer Studio(CCS)
- 4) To Find the addition of Sinusoidal Signals
- 5) To verify Discrete Fourier Transform(DFT) and Inverse Discrete Fourier Transform(IDFT)
  - a) Using MATLAB
  - b) Using Code Composer Studio(CCS)
- 6) Transfer Function Stability Analysis: using pole-zero plot, bode plot, Nyquist plot, z-plane plot.

**PART-2 ( FILTERS )**

- 7) Frequency Response of IIR low pass Butterworth Filter
- 8) Frequency Response of IIR high pass Butterworth Filter
- 9) Frequency Response of IIR low pass Chebyshev Filter
- 10) Frequency Response of IIR high pass Chebyshev Filter
- 11) Frequency Response of FIR low pass Filter using Rectangle Window
- 12) Frequency Response of FIR low pass Filter using Triangle Window

**PART – 3( IMAGE PROCESSING )**

- 13) An image processing in a false contouring system
- 14) To generate the histogram equalization to the image
- 15) To verify the Normalized Cross Correlation to the addition of noise and removal of noise using filters to an image.
- 16) Compute the edge of an image using spatial filters.
- 17) Perform the image motion blur and calculate PSNR to the noise image and also noise free image.
- 18) To verify the PSNR to the Second order Decomposition of Discrete Wavelet transforms and to the reconstructed image using inverse Discrete Wavelet transform

IV Year - II Semester

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## CELLULAR AND MOBILE COMMUNICATIONS

### OBJECTIVES

The student will be introduced to:

1. Understand the basic cellular concepts like frequency reuse, cell splitting, cell sectoring etc., and various cellular systems.
2. Understand the different types of interferences influencing cellular and mobile communications.
3. Understand the frequency management, channel assignment and various propagation effects in cellular environment.
4. Understand the different types antennas used at cell site and mobile.
5. Understand the concepts of handoff and types of handoffs.
6. Understand the architectures of GSM and 3G cellular systems.

### UNIT I

**CELLULAR MOBILE RADIO SYSTEMS:** Introduction to Cellular Mobile System, uniqueness of mobile radio environment, operation of cellular systems, consideration of the components of Cellular system, Hexagonal shaped cells, Analog and Digital Cellular systems.

**CELLULAR CONCEPTS:** Evolution of Cellular systems, Concept of frequency reuse, frequency reuse ratio, Number of channels in a cellular system, Cellular traffic: trunking and blocking, Grade of Service; Cellular structures: macro, micro, pico and femto cells; Cell splitting, Cell sectoring.

### UNIT II

**INTERFERENCE:** Types of interferences, Introduction to Co-Channel Interference, real time Co-Channel interference, Co-Channel measurement, Co-channel Interference Reduction Factor, desired C/I from a normal case in a omni directional Antenna system, design of Antenna system, antenna parameters and their effects, diversity receiver, non-cochannel interference-different types.

### UNIT III

**FREQUENCY MANAGEMENT AND CHANNEL ASSIGNMENT:** Numbering and grouping, setup access and paging channels, channel assignments to cell sites and mobile units: fixed channel and non-fixed channel assignment, channel sharing and borrowing, overlaid cells.

**CELL COVERAGE FOR SIGNAL AND TRAFFIC:** Signal reflections in flat and hilly terrain, effect of human made structures, phase difference between direct and reflected paths, straight line path loss slope, general formula for mobile propagation over water and flat open area, near and long distance propagation, antenna height gain, form of a point to point model.

### UNIT IV

**CELL SITE AND MOBILE ANTENNAS :** Sum and difference patterns and their synthesis, omni directional antennas, directional antennas for interference reduction, space diversity antennas, umbrella pattern antennas, minimum separation of cell site antennas, high gain antennas.

## **UNIT V**

### **HANDOFF STRATEGIES**

Concept of Handoff, types of handoff, handoff initiation, delaying handoff, forced handoff, mobile assigned handoff, intersystem handoff, vehicle locating methods, dropped call rates and their evaluation.

## **UNIT VI**

**DIGITAL CELLULAR NETWORKS:** GSM architecture, GSM channels, multiple access schemes; TDMA, CDMA, OFDMA; architecture of 3G cellular systems.

### **TEXTBOOKS :**

1. Mobile Cellular Telecommunications – W.C.Y. Lee, Tata McGraw Hill, 2<sup>nd</sup> Edn., 2006.
2. Principles of Mobile Communications – Gordon L. Stuber, Springer International 2<sup>nd</sup> Edition, 2007.

### **REFERENCES :**

1. Wireless Communications – Theodore. S. Rappoport, Pearson education, 2<sup>nd</sup> Edn., 2002.
2. Wireless and Mobile Communications – Lee McGraw Hills, 3<sup>rd</sup> Edition, 2006.
3. Mobile Cellular Communication – G Sasibhushana Rao Pearson
3. Wireless Communication and Networking – Jon W. Mark and Weihua Zhqung, PHI, 2005.
4. Wireless Communication Technology – R. Blake, Thompson Asia Pvt. Ltd., 2004.

### **Outcomes:**

#### **At the end of this course the student can able to:**

1. Identify the limitations of conventional mobile telephone systems; understand the concepts of cellular systems.
2. Understand the frequency management, channel assignment strategies and antennas in cellular systems.
3. Understand the concepts of handoff and architectures of various cellular systems.

IV Year - II Semester

<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
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## **ELECTRONIC MEASUREMENTS AND INSTRUMENTATION**

### **UNIT I**

Performance characteristics of instruments, Static characteristics, Accuracy, Resolution, Precision, Expected value, Error, Sensitivity. Errors in Measurement, Dynamic Characteristics-speed of response, Fidelity, Lag and Dynamic error. DC Voltmeters- Multi-range, Range extension/Solid state and differential voltmeters, AC voltmeters- multi range, range extension, shunt. Thermocouple type RF ammeter, Ohmmeters series type, shunt type, Multi-meter for Voltage, Current and resistance measurements.

### **UNIT II**

Signal Generator- fixed and variable, AF oscillators, Standard and AF sine and square wave signal generators, Function Generators, Square pulse, Random noise, sweep, Arbitrary waveform. Wave Analyzers, Harmonic Distortion Analyzers, Spectrum Analyzers, Digital Fourier Analyzers.

### **UNIT III**

Oscilloscopes CRT features, vertical amplifiers, horizontal deflection system, sweep, trigger pulse, delay line, sync selector circuits, simple CRO, triggered sweep CRO, Dual beam CRO, . Dual trace oscilloscope, sampling oscilloscope, storage oscilloscope, digital readout oscilloscope, digital storage oscilloscope, Lissajous method of frequency measurement, standard specifications of CRO, probes for CRO- Active & Passive, attenuator type.

### **UNIT IV**

AC Bridges Measurement of inductance- Maxwell's bridge, Anderson bridge. Measurement of capacitance - Schering Bridge. Wheat stone bridge. Wien Bridge, Errors and precautions in using bridges. Q-meter.

### **UNIT V**

Transducers- active & passive transducers : Resistance, Capacitance, inductance; Strain gauges, LVDT, Piezo Electric transducers, Resistance Thermometers, Thermocouples, Thermistors, Sensistors.

### **UNIT VI**

Measurement of physical parameters force, pressure, velocity, humidity, moisture, speed, proximity and displacement. Data acquisition systems.

### **TEXTBOOKS :**

1. Electronic instrumentation, second edition - H.S.Kalsi, Tata McGraw Hill, 2004.
2. Modern Electronic Instrumentation and Measurement Techniques – A.D. Helfrick and W.D. Cooper, PHI, 5th Edition, 2002.

### **REFERENCES :**

1. Electronic Instrumentation & Measurements - David A. Bell, PHI, 2nd Edition, 2003.
2. Electronic Test Instruments, Analog and Digital Measurements - Robert A.Witte, Pearson Education, 2<sup>nd</sup> Ed., 2004.
3. Electronic Measurements & Instrumentations by K. Lal Kishore, Pearson Education - 2005.

## **OUTCOMES**

The student will be able to

- Select the instrument to be used based on the requirements.
- Understand and analyze different signal generators and analyzers.
- Understand the design of oscilloscopes for different applications.
- Design different transducers for measurement of different parameters.

IV Year - II Semester

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## **SATELLITE COMMUNICATIONS**

### **OBJECTIVES**

**The student will be introduced to:**

1. Understand the basic concepts, applications, frequencies used and types of satellite communications.
2. Understand the concept of look angles, launches and launch vehicles and orbital effects in satellite communications.
3. Understand the various satellite subsystems and its functionality.
4. Understand the concepts of satellite link design and calculation of C/N ratio.
5. Understand the concepts of multiple access and various types of multiple access techniques in satellite systems.
6. Understand the concepts of satellite navigation, architecture and applications of GPS.

### **UNIT I**

**INTRODUCTION [2] :** Origin of Satellite Communications, Historical Back-ground, Basic Concepts of Satellite Communications, Frequency allocations for Satellite Services, Applications, Future Trends of Satellite Communications.

**ORBITAL MECHANICS AND LAUNCHERS[1] :** Orbital Mechanics, Look Angle determination, Orbital perturbations, Orbit determination, launches and launch vehicles, Orbital effects in communication systems performance.

### **UNIT II**

**SATELLITE SUBSYSTEMS[1] :** Attitude and orbit control system, telemetry, tracking, Command and monitoring, power systems, communication subsystems, Satellite antenna Equipment reliability and Space qualification.

### **UNIT III**

**SATELLITE LINK DESIGN[1] :** Basic transmission theory, system noise temperature and G/T ratio, Design of down links, up link design, Design of satellite links for specified C/N, System design example.

### **UNIT IV**

**MULTIPLE ACCESS[1][2] :** Frequency division multiple access (FDMA) Intermodulation, Calculation of C/N. Time division Multiple Access (TDMA) Frame structure, Examples. Satellite Switched TDMA Onboard processing, DAMA, Code Division Multiple access (CDMA), Spread spectrum transmission and reception.

### **UNIT V**

**EARTH STATION TECHNOLOGY[3] :** Introduction, Transmitters, Receivers, Antennas, Tracking systems, Terrestrial interface, Primary power test methods.

**LOW EARTH ORBIT AND GEO-STATIONARY SATELLITE SYSTEMS[1] :** Orbit consideration, coverage and frequency considerations, Delay & Throughput considerations, System considerations, Operational NGSO constellation Designs

## **UNIT VI**

**SATELLITE NAVIGATION & THE GLOBAL POSITIONING SYSTEM [1] :** Radio and Satellite Navigation, GPS Position Location principles, GPS Receivers and codes, Satellite signal acquisition, GPS Navigation Message, GPS signal levels, GPS receiver operation, GPS C/A code accuracy, Differential GPS.

### **TEXT BOOKS:**

1. Satellite Communications – Timothy Pratt, Charles Bostian and Jeremy Allnut, WSE, Wiley Publications, 2nd Edition, 2003.
2. Satellite Communications Engineering – Wilbur L. Pritchard, Robert A Nelson and Henri G.Suyderhoud, 2nd Edition, Pearson Publications, 2003.

### **REFERENCES :**

1. Satellite Communications : Design Principles – M. Richharia, BS Publications, 2nd Edition, 2003.
2. Satellite Communication - D.C Agarwal, Khanna Publications, 5th Ed.
3. Fundamentals of Satellite Communications – K.N. Raja Rao, PHI, 2004
4. Satellite Communications – Dennis Roddy, McGraw Hill, 2nd Edition, 1996.

### **Outcomes:**

#### **At the end of this course the student can able to:**

1. Understand the concepts, applications and subsystems of Satellite communications.
2. Derive the expression for G/T ratio and to solve some analytical problems on satellite link design.
3. Understand the various types of multiple access techniques and architecture of earth station design.
4. Understand the concepts of GPS and its architecture.

IV Year - II Semester

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**WIRELESS SENSORS AND NETWORKS  
ELECTIVE-III**

**UNIT I**

**OVERVIEW OF WIRELESS SENSOR NETWORKS:**

Key definitions of sensor networks, Advantages of sensor Networks, Unique constraints and challenges, Driving Applications, Enabling Technologies for Wireless Sensor Networks.

**ARCHITECTURES:**

Single-Node Architecture - Hardware Components, Energy Consumption of Sensor Nodes, Operating Systems and Execution Environments, Network Architecture -Sensor Network Scenarios, Optimization Goals and Figures of Merit, Gateway Concepts.

**UNIT II**

**NETWORKING Technologies:**

Physical Layer and Transceiver Design Considerations, Personal area networks (PANs), hidden node and exposed node problem, Topologies of PANs, MANETs, WANETs.

**UNIT-III**

**MAC Protocols for Wireless Sensor Networks:**

Issues in Designing a MAC protocol for Ad Hoc Wireless Networks, Design goals of a MAC Protocol for Ad Hoc Wireless Networks, Classifications of MAC Protocols, Contention - Based Protocols, Contention - Based Protocols with reservation Mechanisms, Contention – Based MAC Protocols with Scheduling Mechanisms, MAC Protocols that use Directional Antennas, Other MAC Protocols.

**UNIT-IV**

**ROUTING PROTOCOLS:**

Introduction, Issues in Designing a Routing Protocol for Ad Hoc Wireless Networks, Classification of Routing Protocols, Table –Driven Routing Protocols, On – Demand Routing Protocols, Hybrid Routing Protocols, Routing Protocols with Efficient Flooding Mechanisms, Hierarchical Routing Protocols, Power – Aware Routing Protocols, Proactive Routing

**UNIT-V**

**TRANSPORT LAYER AND SECURITY PROTOCOLS:**

Introduction, Issues in Designing a Transport Layer Protocol for Ad Hoc Wireless Networks, Design Goals of a Transport Layer Protocol for Ad Hoc Wireless Networks, Classification of Transport Layer Solutions, TCP Over Ad Hoc Wireless Networks, Other Transport Layer Protocol for Ad Hoc Wireless Networks,

**UNIT- VI**

**SECURITY IN WSNs:**

Security in Ad Hoc Wireless Networks, Network Security Requirements, Issues and Challenges in Security Provisioning, Network Security Attacks, Key Management, Secure Routing in Ad Hoc Wireless Networks.

**SENSOR NETWORK PLATFORMS AND TOOLS:**

Sensor Node Hardware – Berkeley Motes, Programming Challenges, Node-level software platforms, Node-level Simulators, State-centric programming.

**APPLICATIONS of WSN:**

S Ultra wide band radio communication, Wireless fidelity systems. Future directions, Home automation, smart metering Applications



**TEXT BOOKS:**

1. Ad Hoc Wireless Networks: Architectures and Protocols - C. Siva Ram Murthy and B.S.Manoj, 2004, PHI
2. Wireless Ad- hoc and Sensor Networks: Protocols, Performance and Control – Jagannathan Sarangapani, CRC Press
3. Holger Karl & Andreas Willig, “Protocols And Architectures for Wireless Sensor Networks”, John Wiley, 2005.

**REFERENCES:**

1. Kazem Sohraby, Daniel Minoli, & Taieb Znati, “Wireless Sensor Networks- Technology, Protocols, and Applications”, John Wiley, 2007.
2. Feng Zhao & Leonidas J. Guibas, “Wireless Sensor Networks- An Information Processing Approach”, Elsevier, 2007.
3. Ad- Hoc Mobile Wireless Networks: Protocols & Systems, C.K. Toh ,1 ed. Pearson Education.
4. Wireless Sensor Networks - C. S. Raghavendra, Krishna M. Sivalingam, 2004, Springer
5. Wireless Sensor Networks – S Anandamurugan , Lakshmi Publications

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## **DIGITAL IC DESIGN**

### **ELECTIVE-III**

#### **OBJECTIVES**

- The student will be able to understand the MOS Design.
- In this course, students can study Combinational MOS Logic Circuits and Sequential MOS Logic Circuits.
- Another main object of this course is to motivate the graduate students to design and to develop the Digital Integrated Circuits for different Applications.
- The concepts of Semiconductor Memories, Flash Memory, RAM array organization.

#### **UNIT-I:**

**MOS Design:** Pseudo NMOS Logic – Inverter, Inverter threshold voltage, Output high voltage, Output Low voltage, Gain at gate threshold voltage, Transient response, Rise time, Fall time, Pseudo NMOS logic gates, Transistor equivalency, CMOS Inverter logic.

#### **UNIT-II:**

**Combinational MOS Logic Circuits:** MOS logic circuits with NMOS loads, Primitive CMOS logic gates – NOR & NAND gate, Complex Logic circuits design – Realizing Boolean expressions using NMOS gates and CMOS gates, AOI and OIA gates, CMOS full adder, CMOS transmission gates, Designing with Transmission gates.

#### **UNIT-III:**

**Sequential MOS Logic Circuits:** Behaviour of bistable elements, SR Latch, Clocked latch and flip flop circuits, CMOS D latch and edge triggered flip-flop.

#### **UNIT-IV:**

**Dynamic Logic Circuits:** Basic principle, Voltage Bootstrapping, Synchronous dynamic pass transistor circuits, Dynamic CMOS transmission gate logic, High performance Dynamic CMOS circuits.

#### **UNIT-V:**

**Interconnect:** Capacitive Parasitics, Resistive Parasitics, Inductive Parasitics, Advanced Interconnect Techniques.

#### **UNIT-VI:**

**Semiconductor Memories:** Memory Types, RAM array organization, DRAM – Types, Operation, Leakage currents in DRAM cell and refresh operation, SRAM operation Leakage currents in SRAM cells, Flash Memory- NOR flash and NAND flash.

**Text Books:**

1. Digital Integrated Circuits – A Design Perspective, Jan M. Rabaey, Anantha Chandrakasan, Borivoje Nikolic, 2<sup>nd</sup> Ed., PHI.
2. Digital Integrated Circuit Design – Ken Martin, Oxford University Press, 2011.

**References:**

1. CMOS Digital Integrated Circuits Analysis and Design – Sung-Mo Kang, Yusuf Leblebici, TMH, 3<sup>rd</sup> Ed., 2011.
2. CMOS VLSI Design – Neil H.E Weste, David harris, Ayan Banerjee 3<sup>rd</sup> Edition, Pearson

**OUTCOMES**

After going through this course the student will be able to

- Understand the concepts of MOS Design.
- Design and analysis of Combinational and Sequential MOS Circuits.
- Extend the Digital IC Design to Different Applications.
- Understand the Concepts of Semiconductor Memories, Flash Memory, RAM array organization.

## OPERATING SYSTEMS ELECTIVE-III

### OBJECTIVES:

- Study the basic concepts and functions of operating systems.
- Understand the structure and functions of OS.
- Learn about Processes, Threads and Scheduling algorithms.
- Understand the principles of concurrency and Deadlocks.
- Learn various memory management schemes.
- Study I/O management and File systems.
- Learn the basics of Linux system and perform administrative tasks on Linux Servers.

### UNIT I

**Introduction to Operating System Concept:** Types of operating systems, operating systems concepts, operating systems services, Introduction to System call, System call types.

### UNIT-II:

**Process Management** – Process concept, The process, Process State Diagram , Process control block, Process Scheduling- Scheduling Queues, Schedulers, Operations on Processes, Interprocess Communication, Threading Issues, Scheduling-Basic Concepts, Scheduling Criteria, Scheduling Algorithms.

### UNIT-III:

**Memory Management:** Swapping, Contiguous Memory Allocation, Paging, structure of the Page Table, Segmentation

**Virtual Memory Management:**

Virtual Memory, Demand Paging, Page-Replacement Algorithms, Thrashing

### UNIT-IV:

**Concurrency:** Process Synchronization, The Critical- Section Problem, Synchronization Hardware, Semaphores, Classic Problems of Synchronization, Monitors, Synchronization examples

**Principles of deadlock** – System Model, Deadlock Characterization, Deadlock Prevention, Detection and Avoidance, Recovery form Deadlock

### UNIT-V:

**File system Interface-** the concept of a file, Access Methods, Directory structure, File system mounting, file sharing, protection.

**File System implementation-** File system structure, allocation methods, free-space management

**Mass-storage structure** overview of Mass-storage structure, Disk scheduling, Device drivers,

### UNIT VI:

**Linux System:** Components of LINUX, Interprocess Communication, Synchronisation, Interrupt, Exception and System Call.

**Android Software Platform:** Android Architecture, Operating System Services, Android Runtime Application Development, Application Structure, Application Process management

**OUTCOMES:**

- Design various Scheduling algorithms.
- Apply the principles of concurrency.
- Design deadlock, prevention and avoidance algorithms.
- Compare and contrast various memory management schemes.
- Design and Implement a prototype file systems.
- Perform administrative tasks on Linux Servers
- Introduction to Android Operating System Internals

**TEXT BOOK:**

1. Operating System Concepts, Abraham Silberschatz, Peter Baer Galvin and Greg Gagne 9th Edition, John Wiley and Sons Inc., 2012.
2. Operating Systems – Internals and Design Principles, William Stallings, 7th Edition, Prentice Hall, 2011.
3. Operating Systems-S Halder, Alex A Aravind Pearson Education Second Edition 2016 .

**REFERENCES:**

1. Modern Operating Systems, Andrew S. Tanenbaum, Second Edition, Addison Wesley, 2001.
2. Operating Systems: A Design-Oriented Approach, Charles Crowley, Tata Mc Graw Hill Education”, 1996.
3. Operating Systems: A Concept-Based Approach, D M Dhamdhare, Second Edition, Tata Mc Graw-Hill Education, 2007.